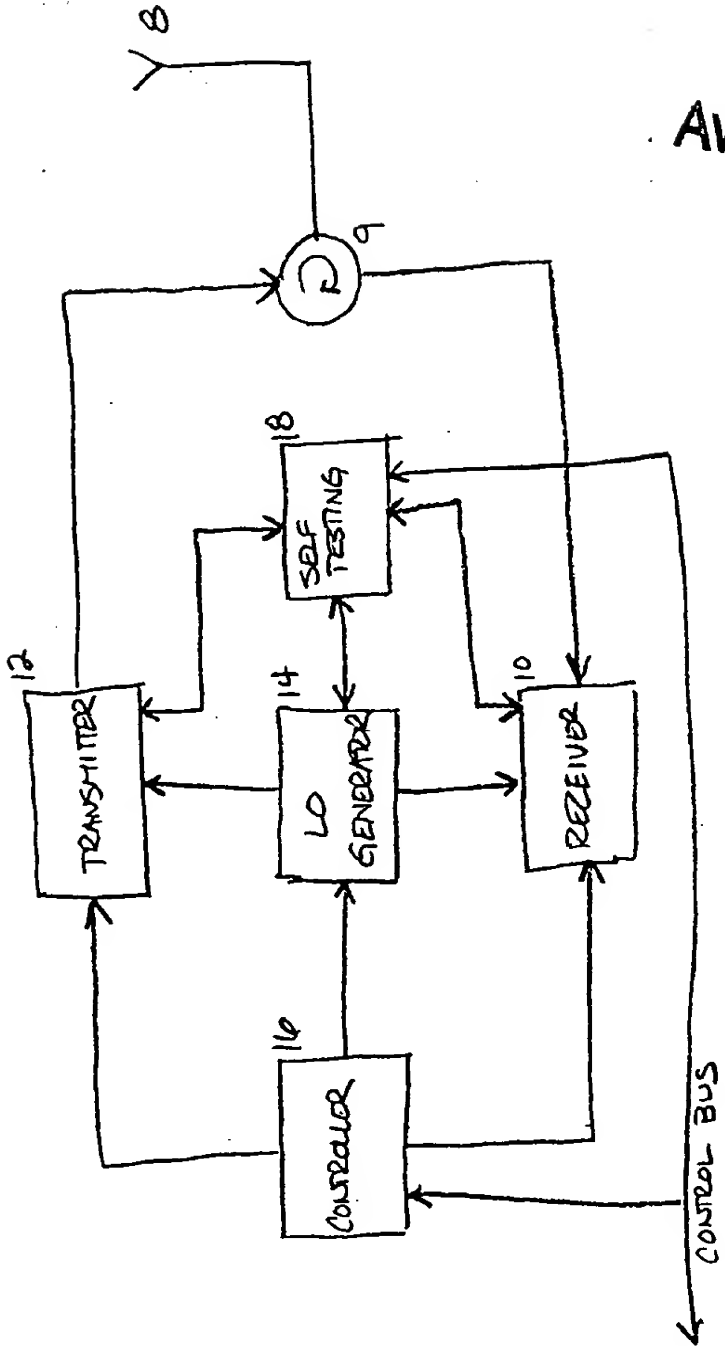


Available Copy

FIG. 1A



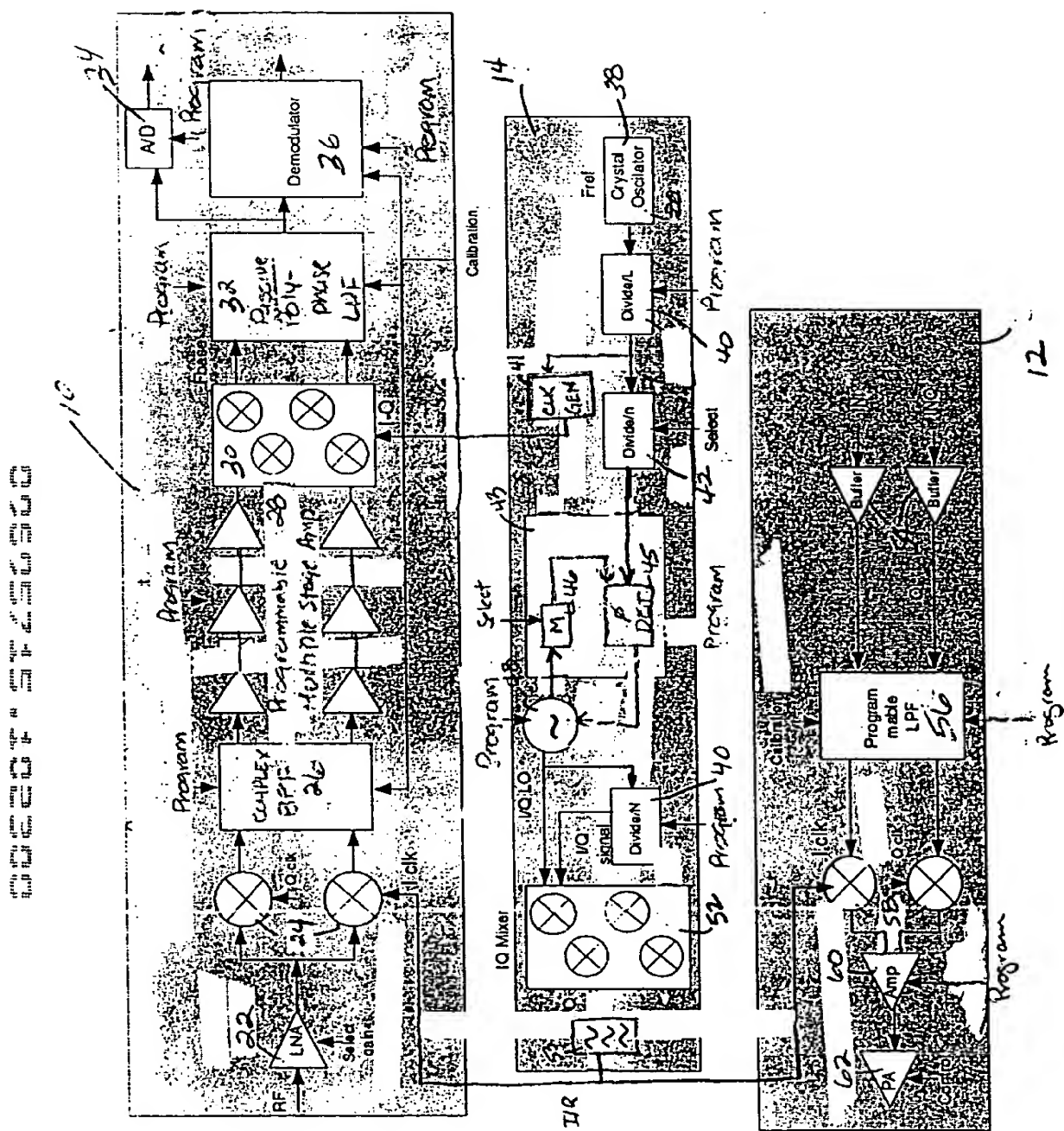


Fig. 2

FIG. 3

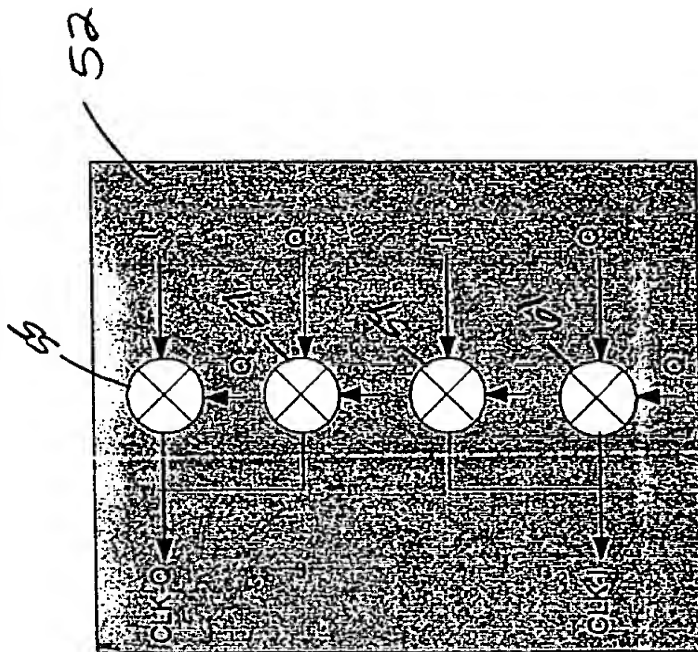


FIG. 3

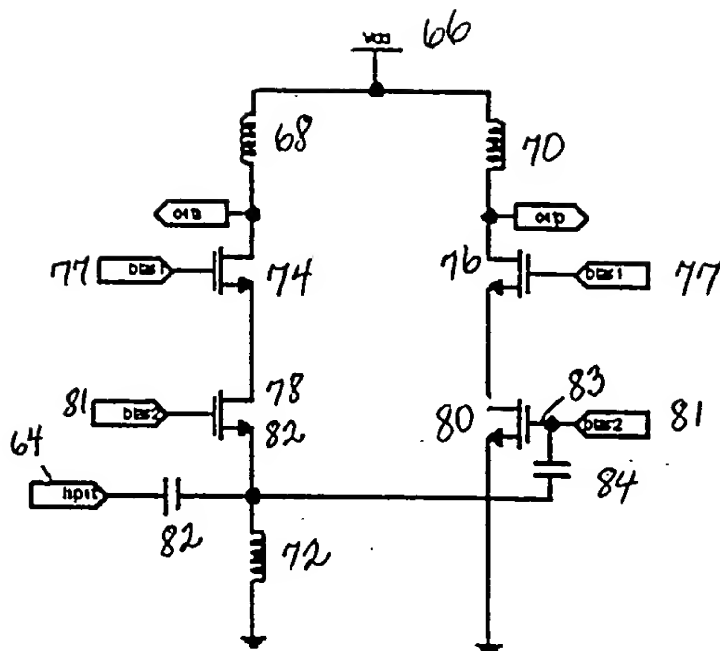


FIG. 4

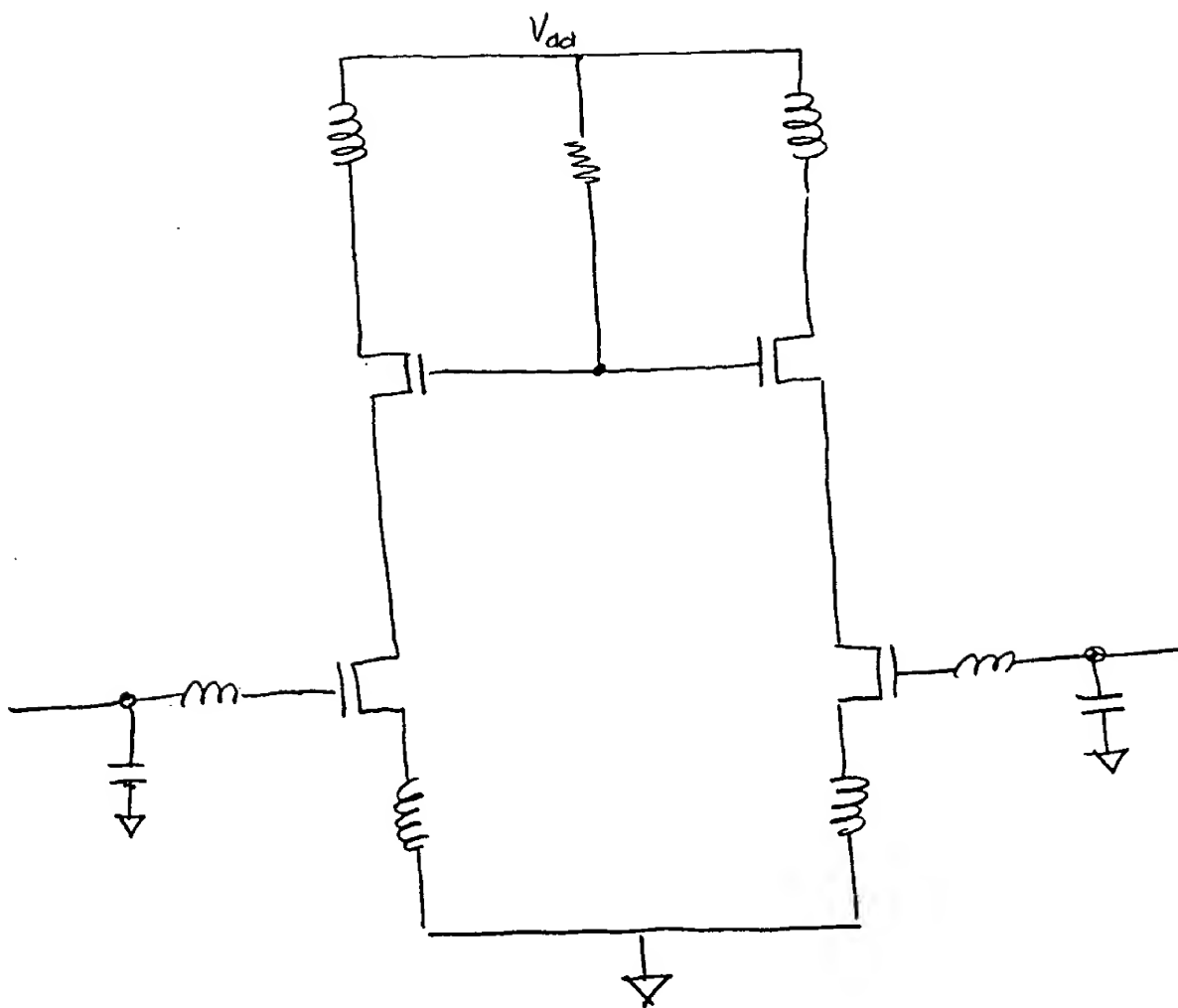


FIG. 4(a)

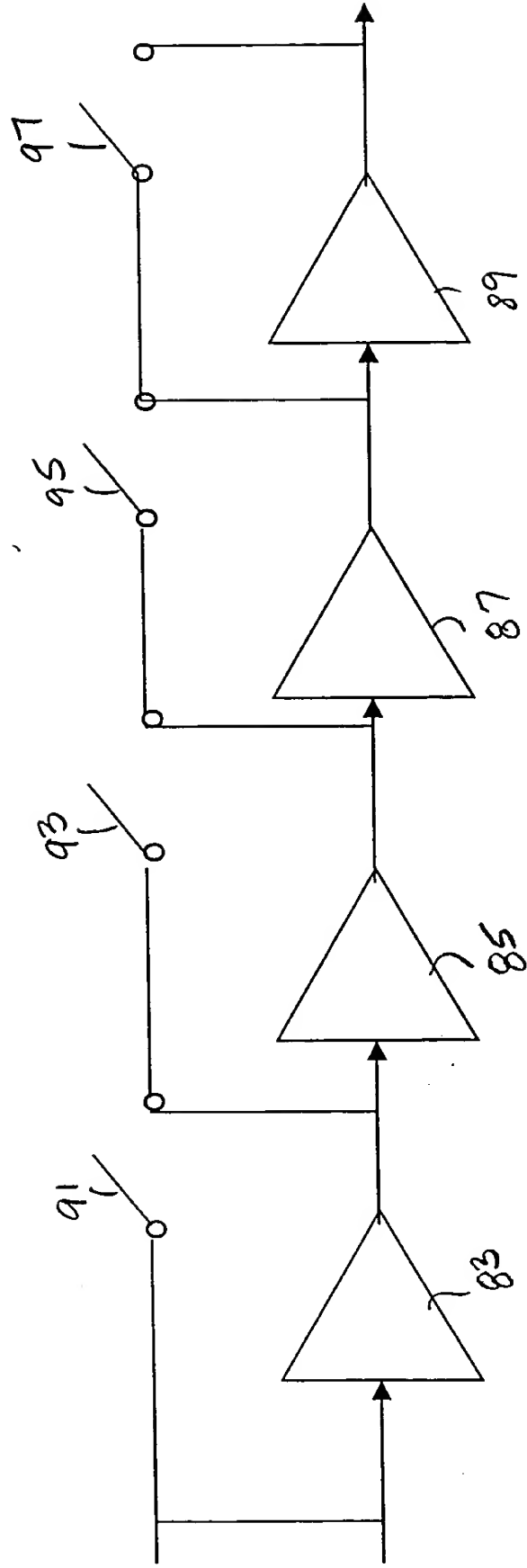


FIG. 5

FIG. 6

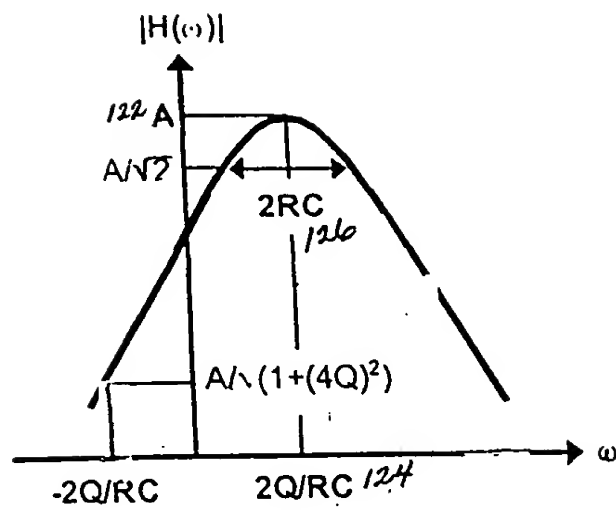


FIG. 7

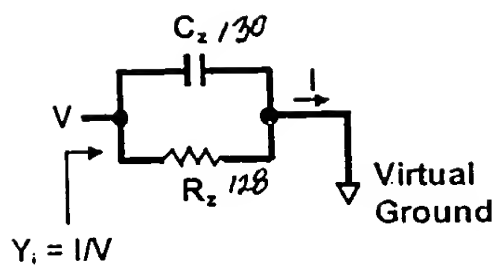


FIG. 8

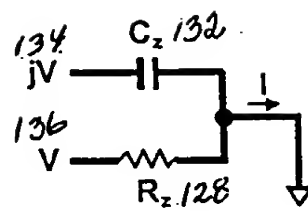


FIG. 9

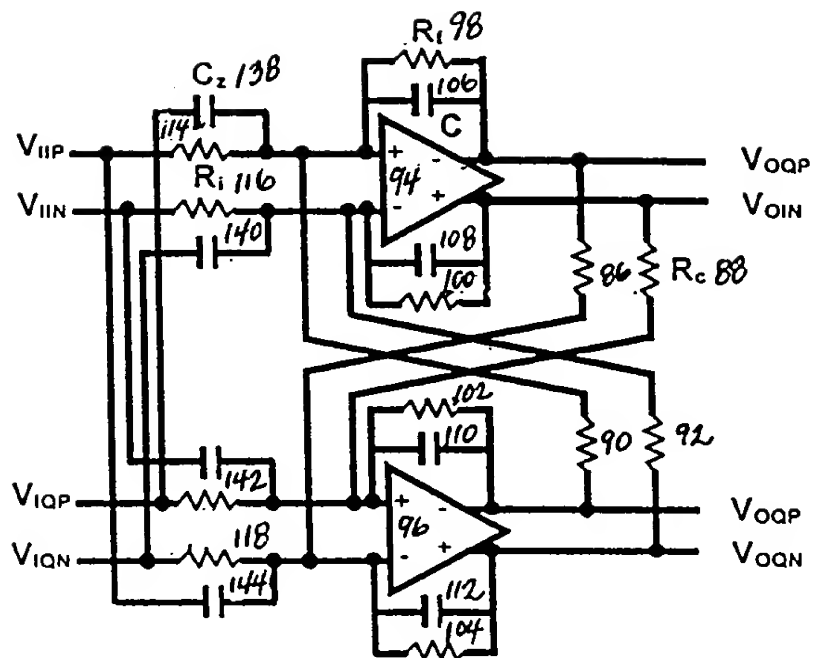


FIG. 10

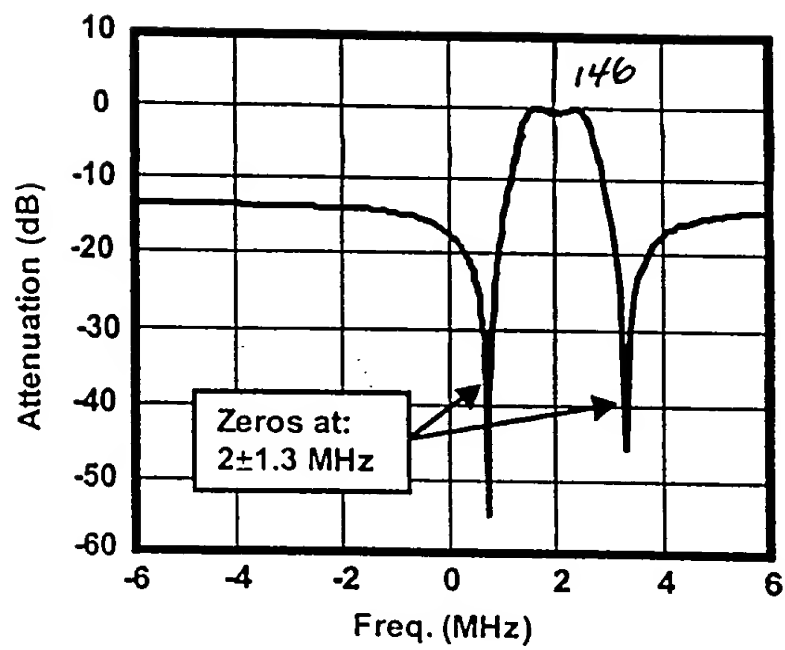


FIG. 11

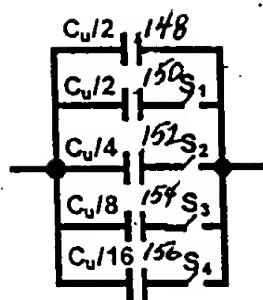


FIG. 12(a)

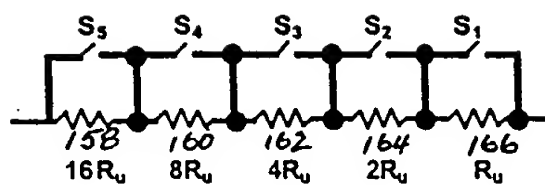


FIG. 12(b)

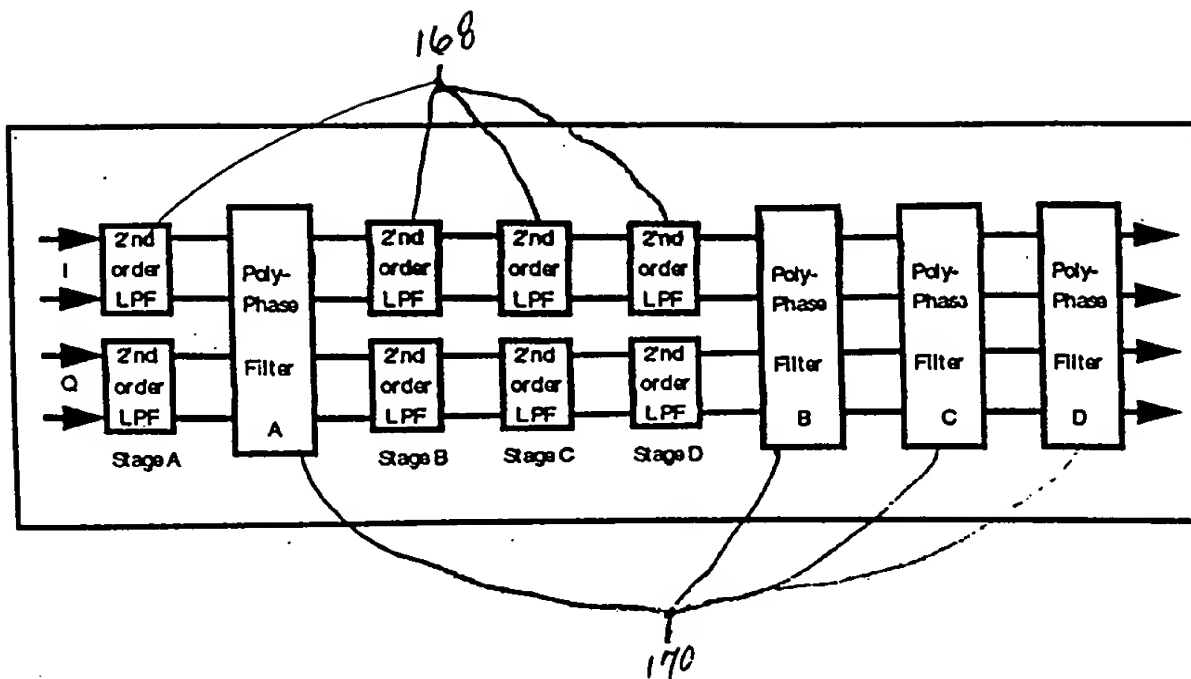


FIG. 13

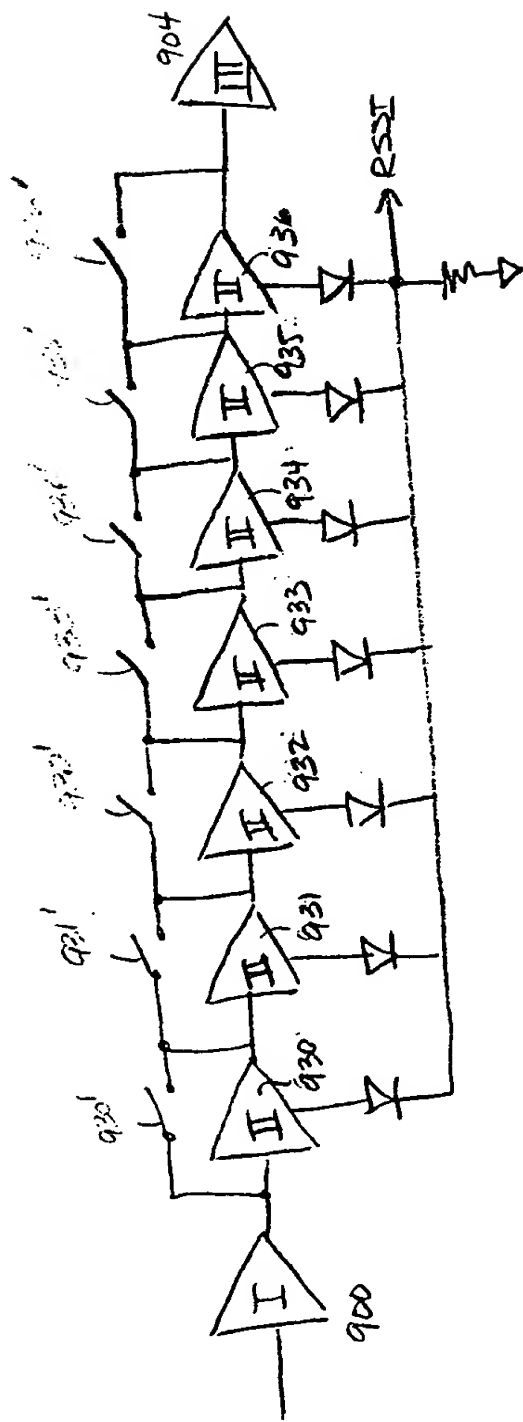
[illegible]

FIG. 14

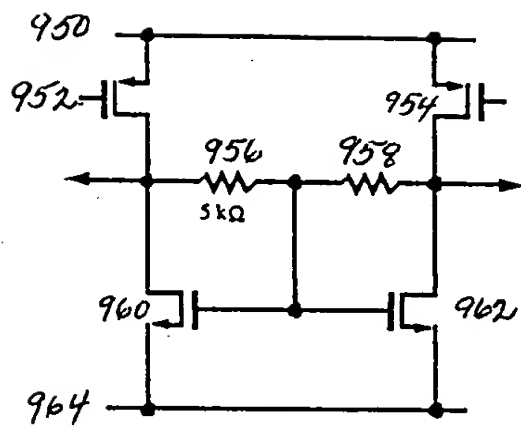


FIG. 15

FIG. 16(a)

↗

FIG. 17(a)

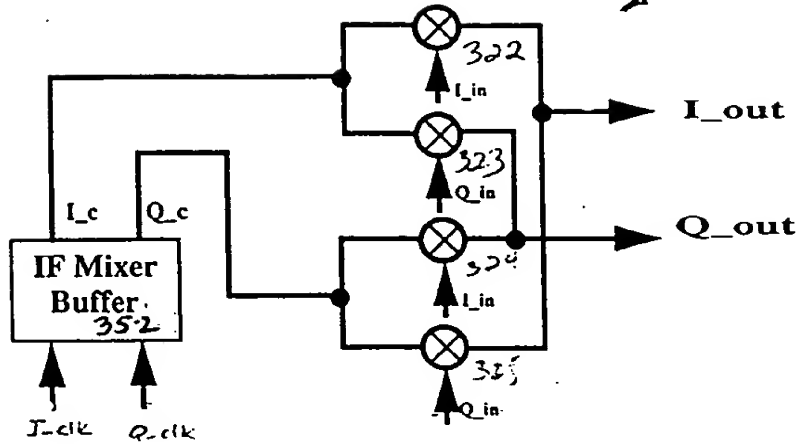


FIG. 17(b)

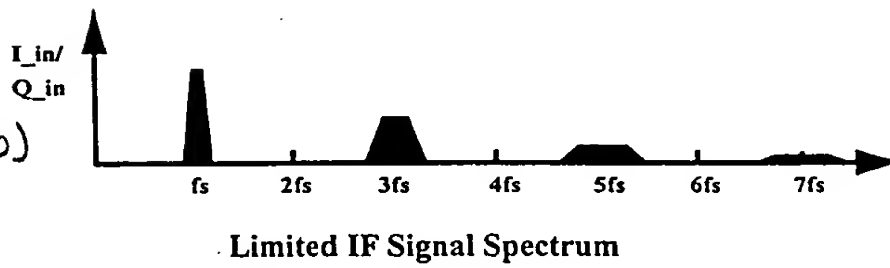


FIG. 17(c)

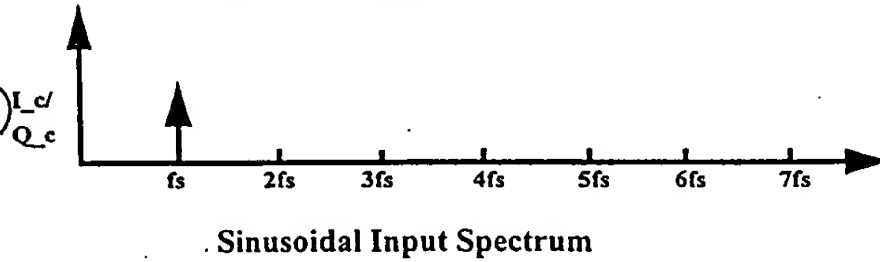


FIG. 17(d)

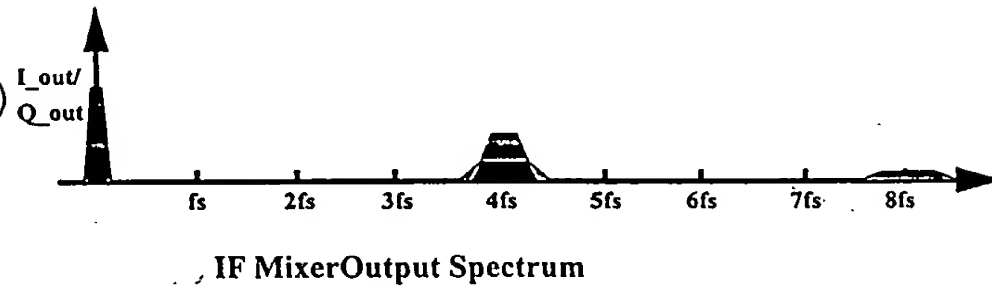


FIG. 18

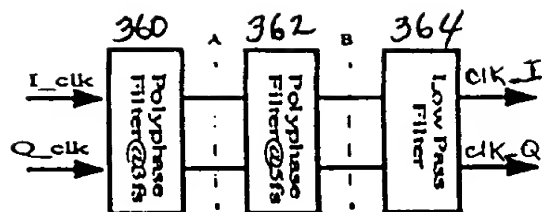


FIG. 19(a)

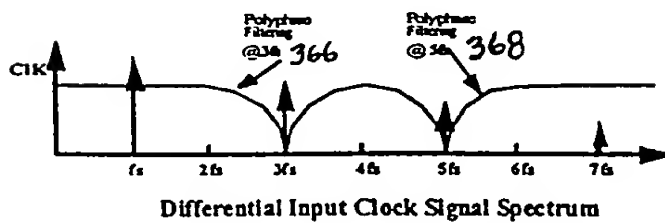


FIG. 19(b)

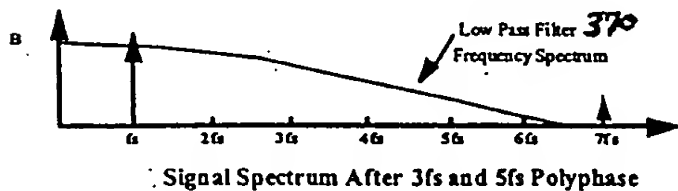


FIG. 19(c)

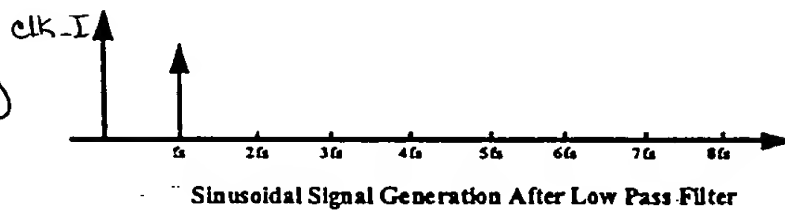


FIG. 19d is a schematic diagram of a circuit 326.

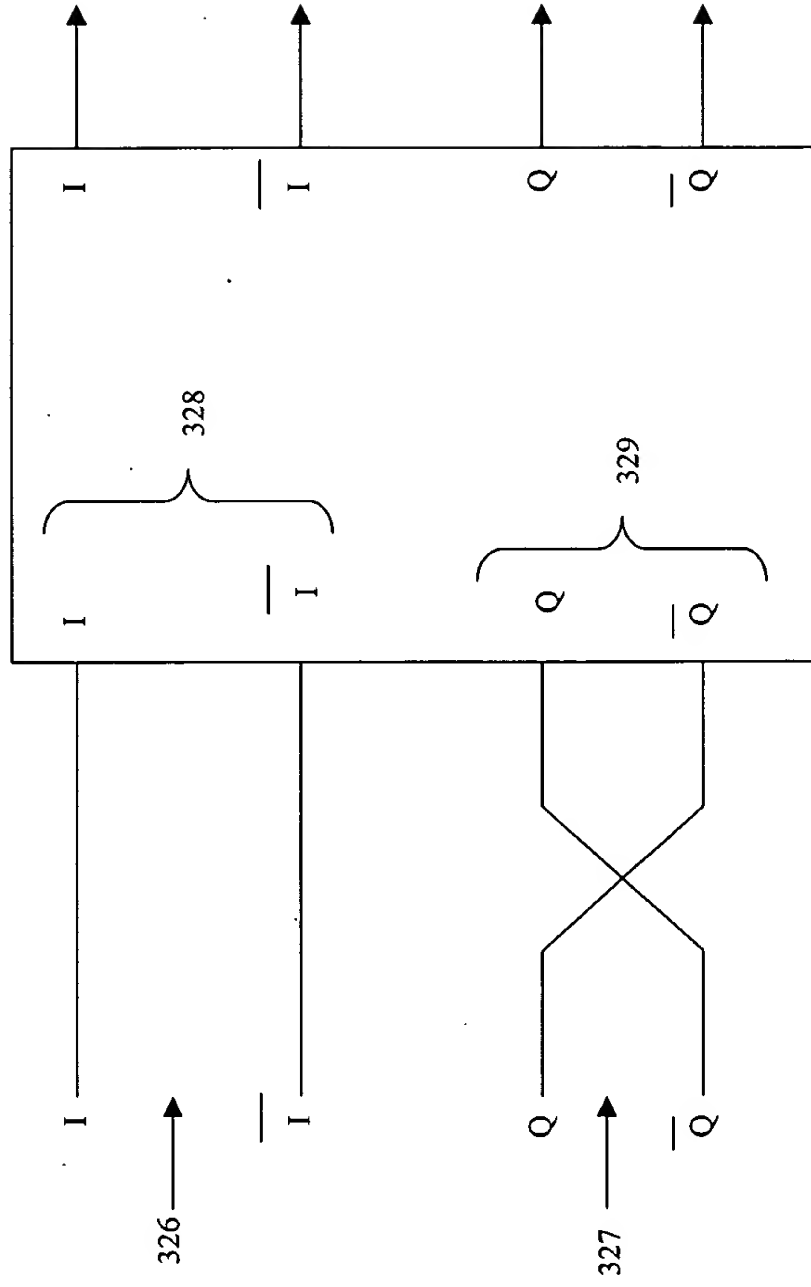


FIG. 19d

FIG. 19e is a schematic diagram of a system 332, which includes two processing blocks 333 and 334. Each block has two inputs labeled 'I' and two outputs labeled 'Q'. The outputs of block 333 are connected to the inputs of block 334. The outputs of block 334 are connected to the inputs of block 333, forming a cross-connection. The system 332 is shown with two input lines on the left and two output lines on the right.

332

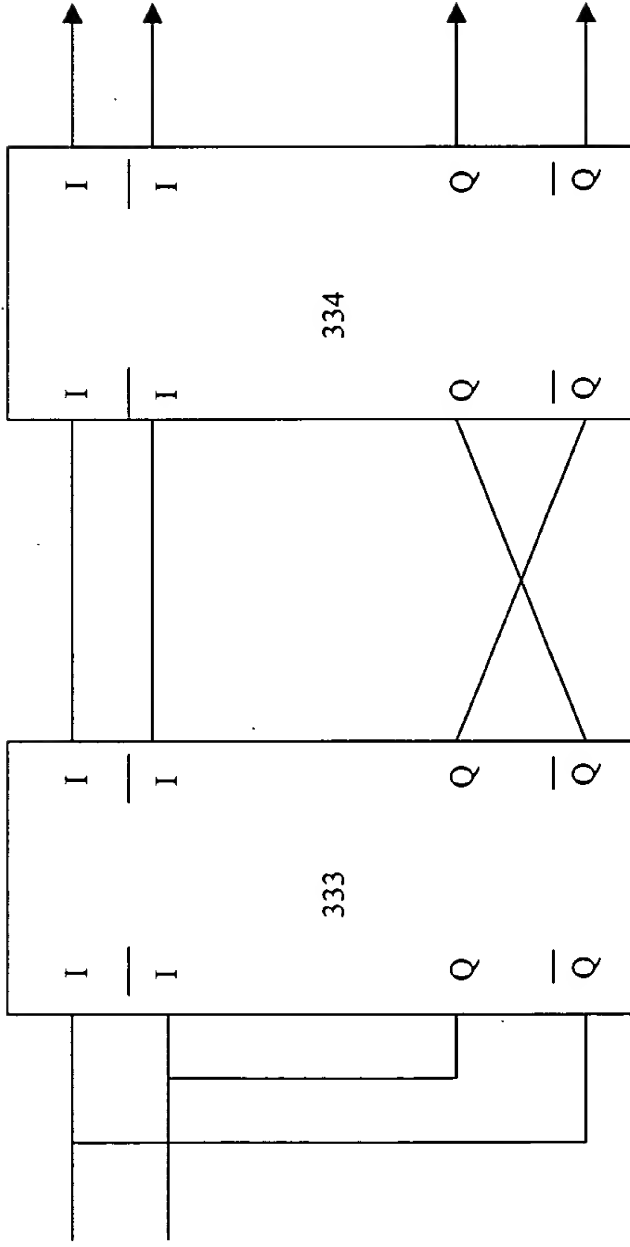


FIG. 19e

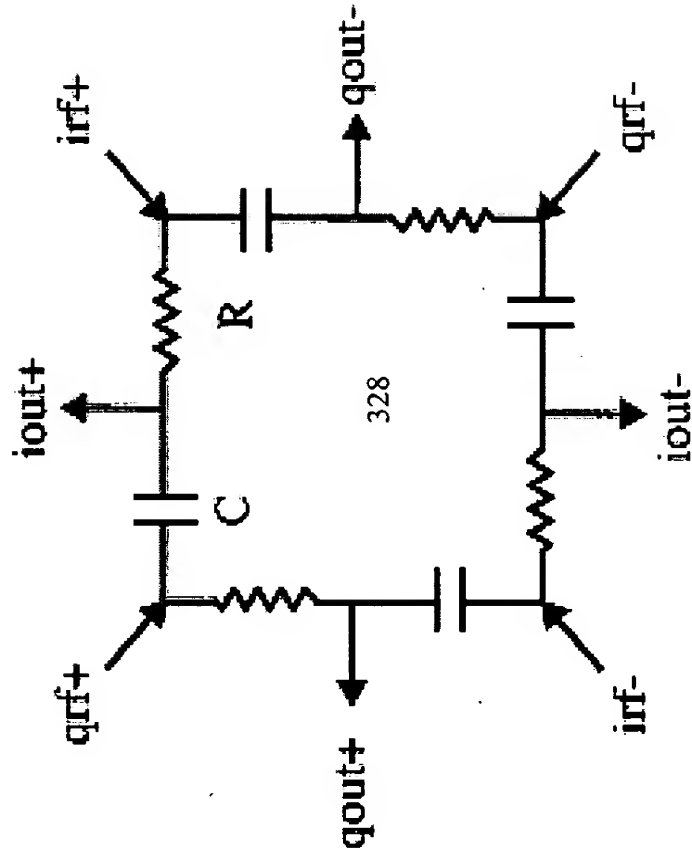
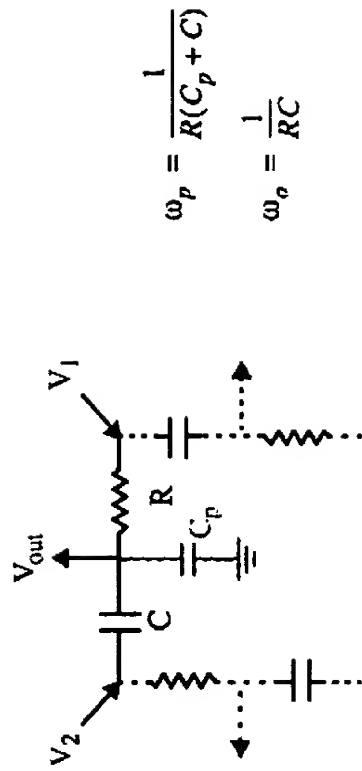


FIG. 19f



$$\omega_p = \frac{1}{R(C_p + C)}$$

$$\omega_o = \frac{1}{RC}$$

$$V_{out} = \frac{V_1}{R(C_p + C)s + 1} + \frac{V_2 RCs}{R(C_p + C)s + 1}$$



FIG. 19g

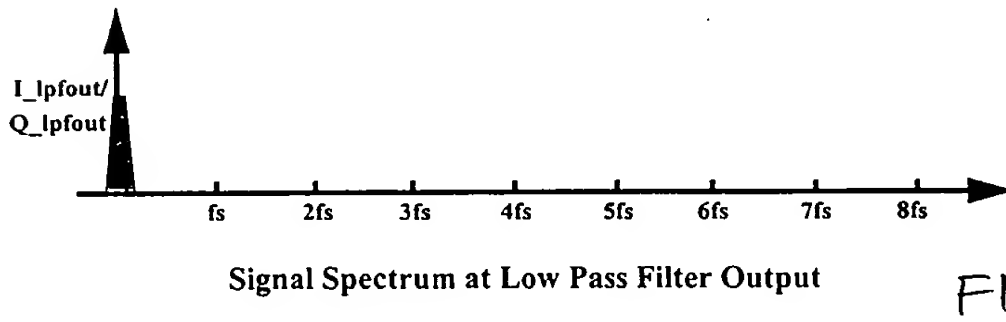
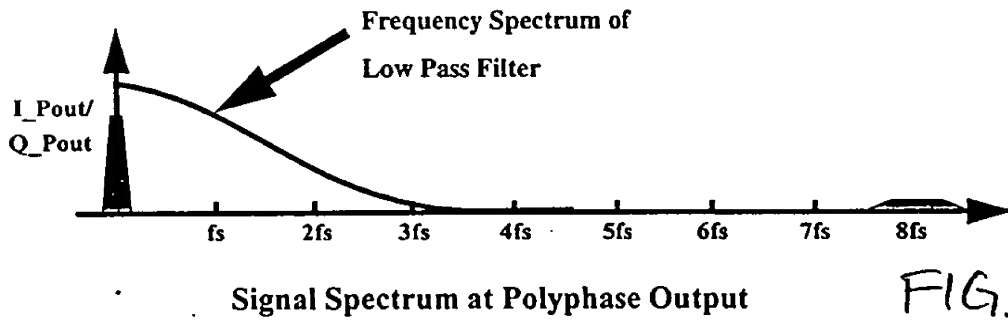
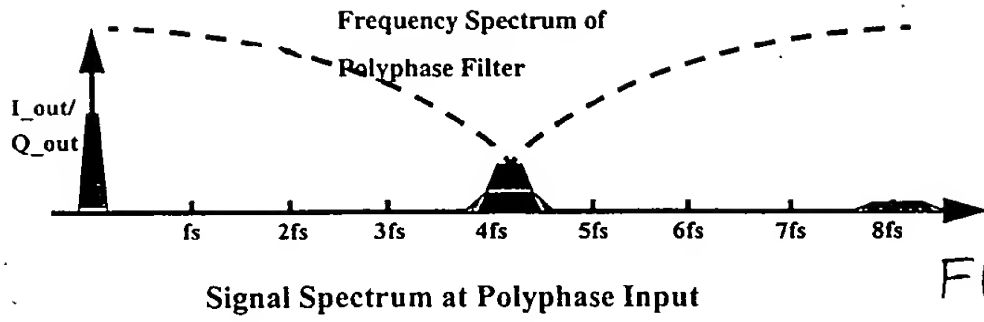


FIG. 21

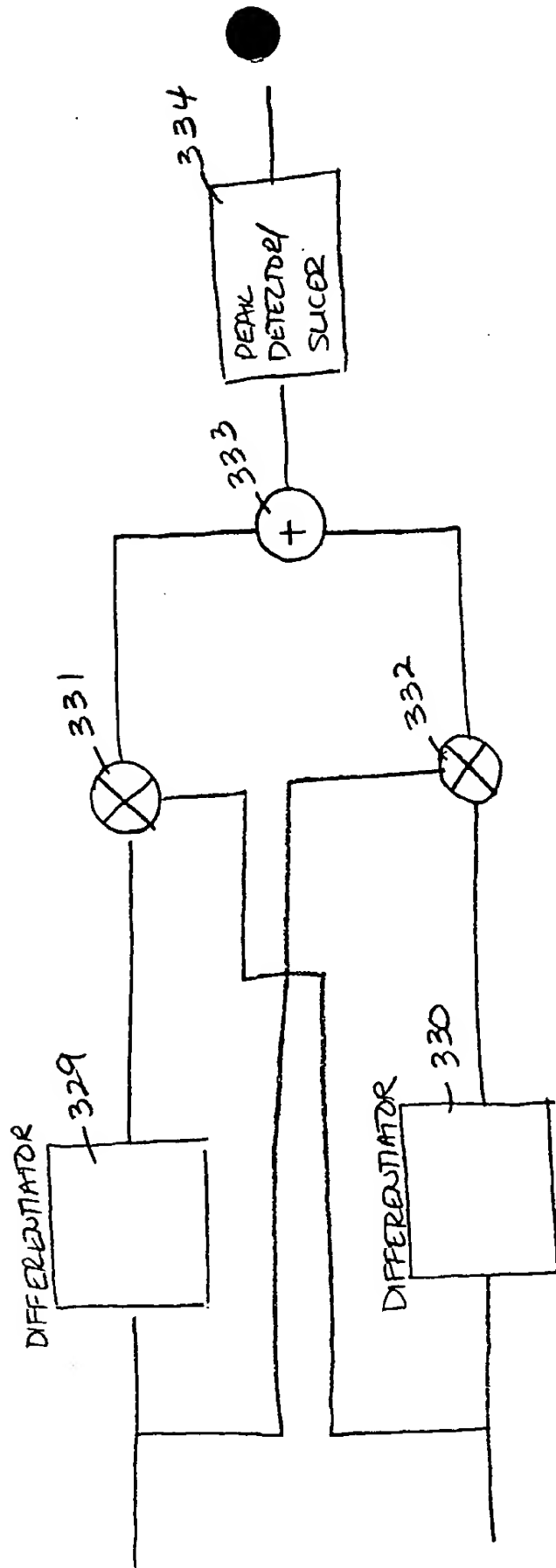


FIG. 21

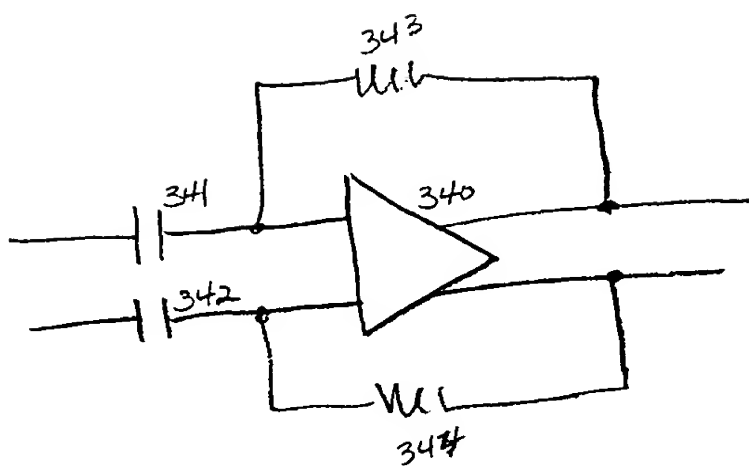


FIGURE 22

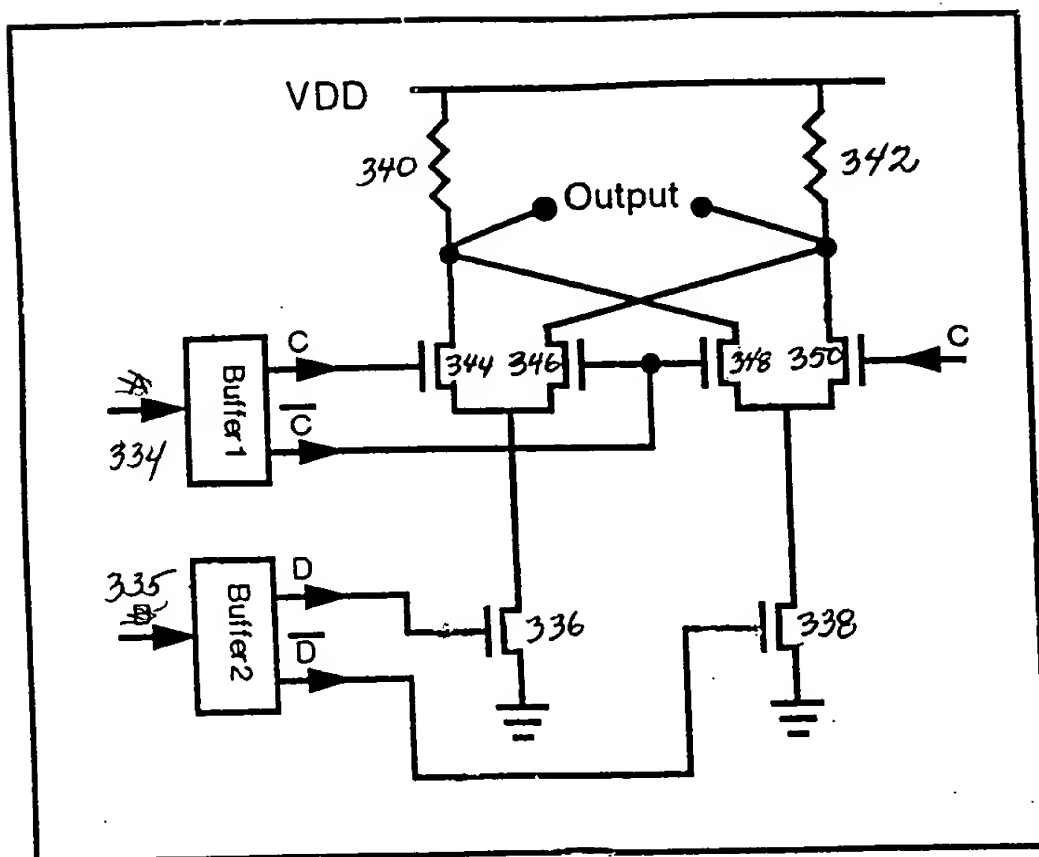


FIG. 23

SECRET

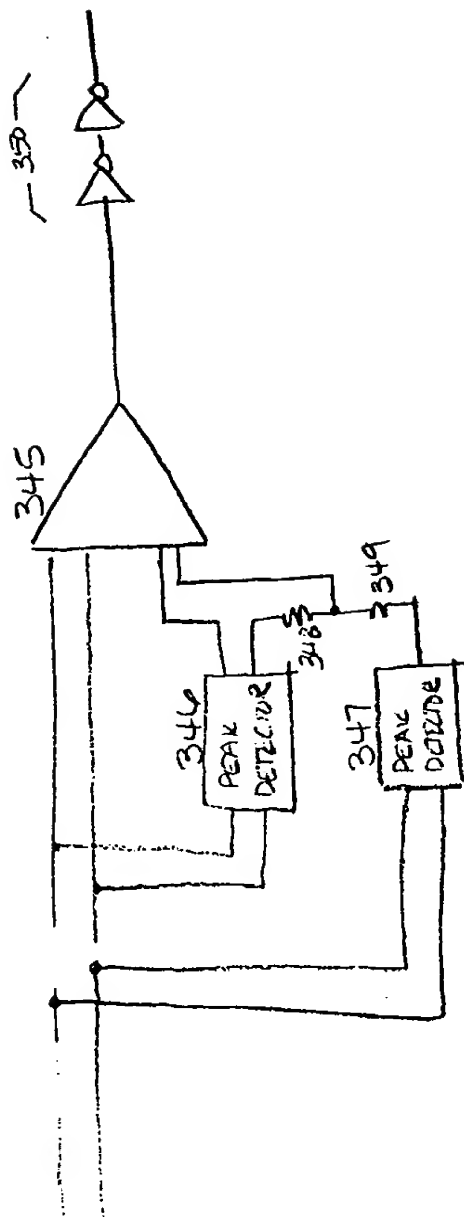


FIGURE 24

FIG. 26(a)

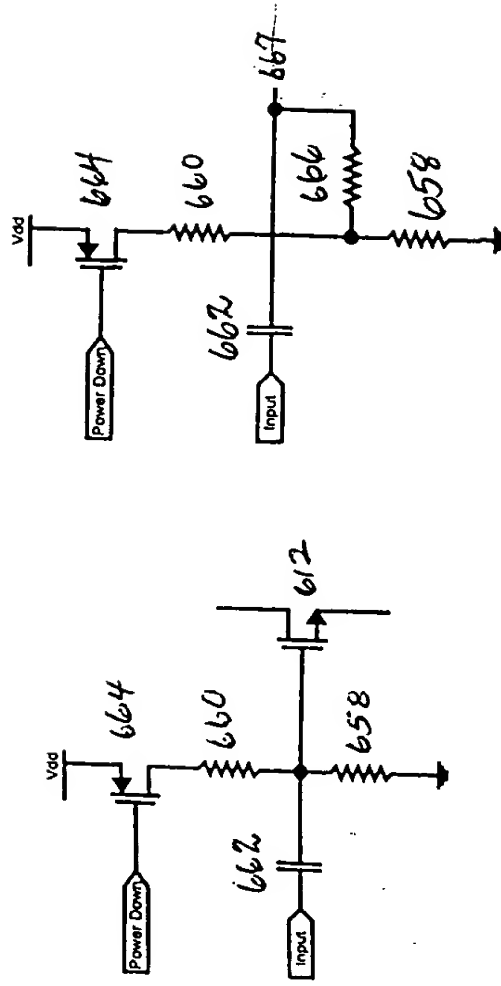


FIG. 26(a)

FIG. 26(b)

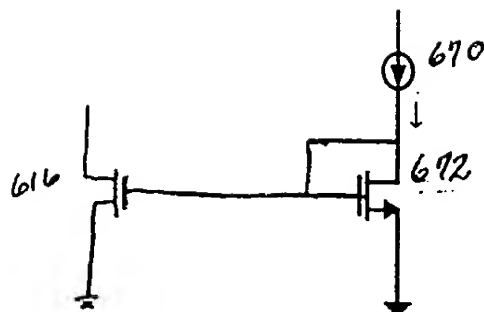


FIG. 27

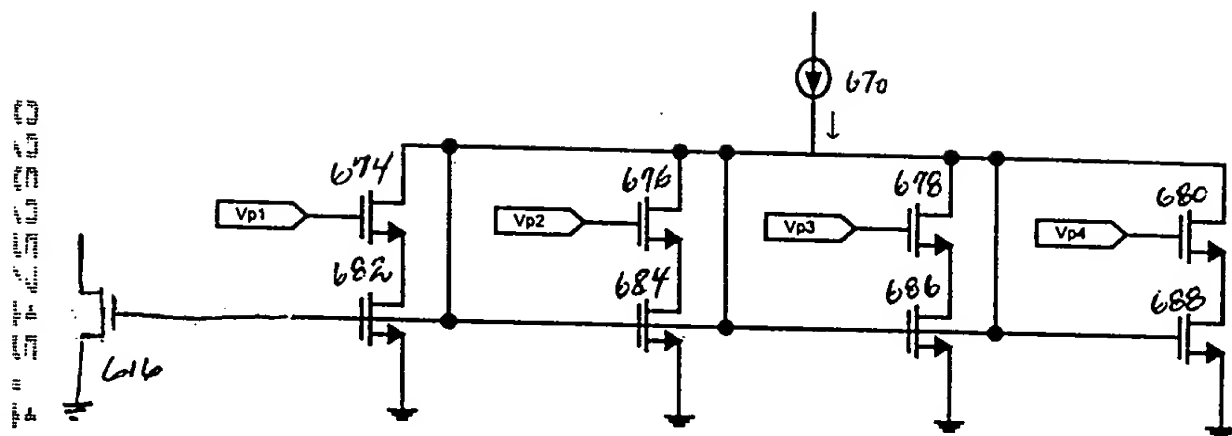


FIG. 28

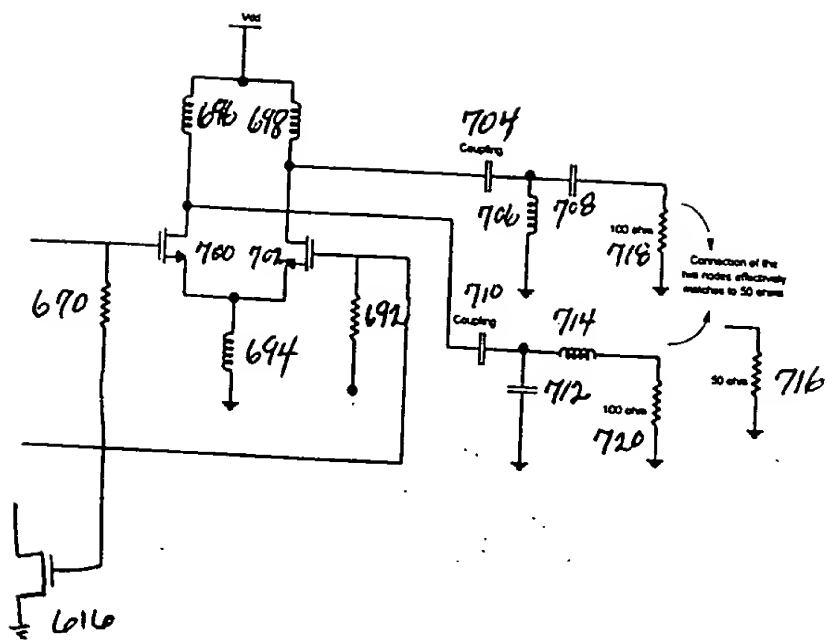


FIG. 29

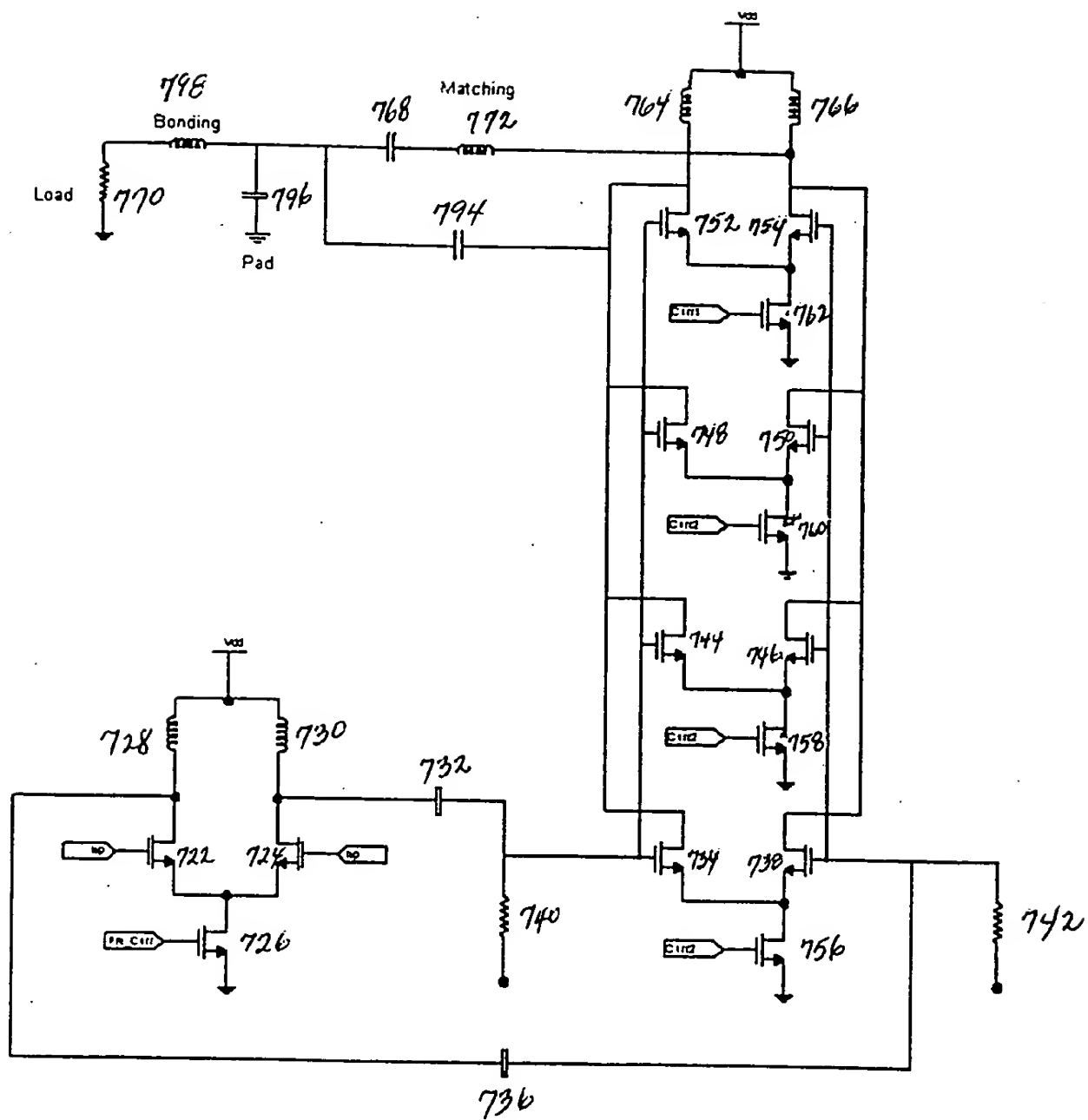


FIG. 30A

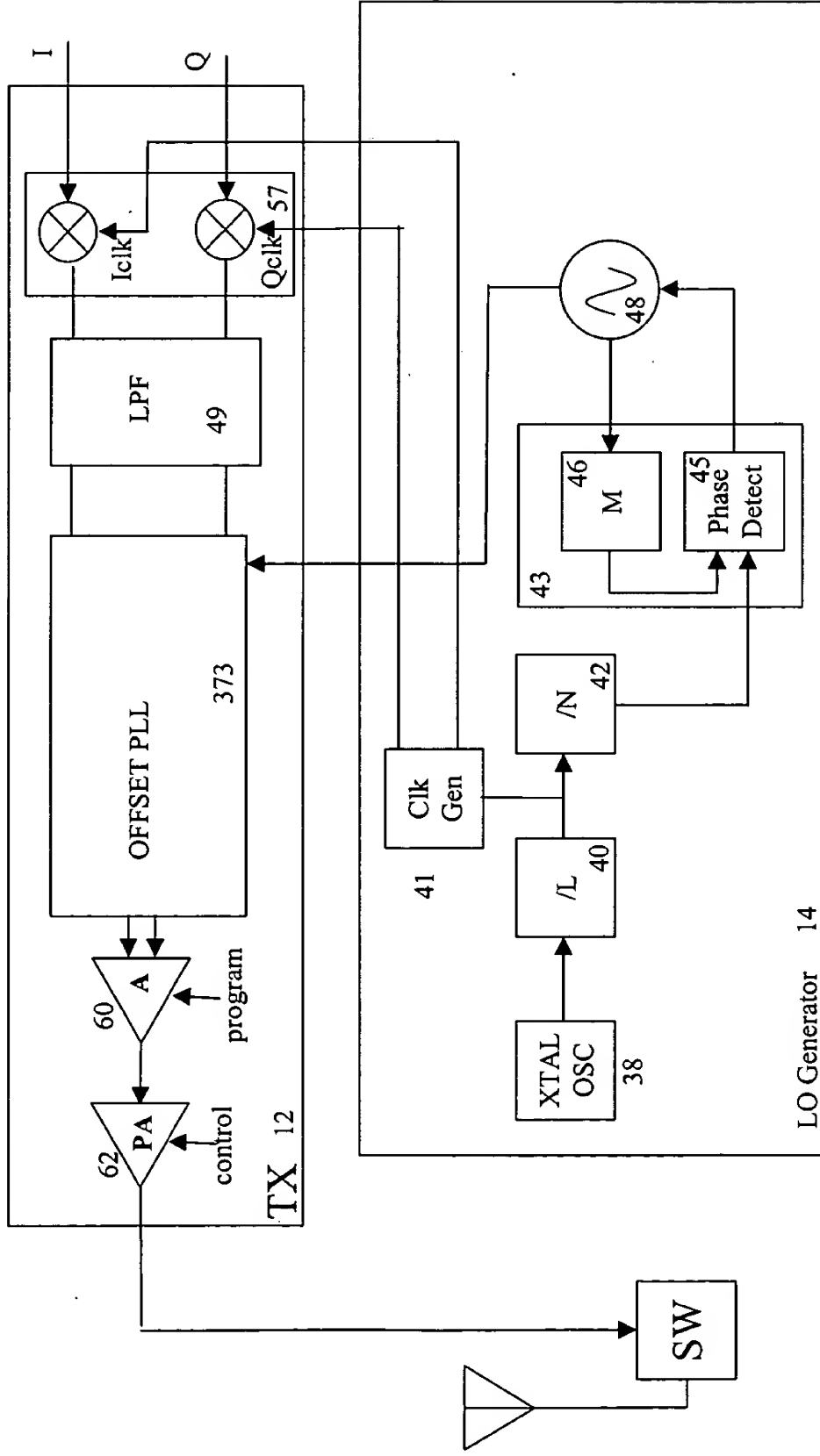


FIG. 30b

FIG. 30c

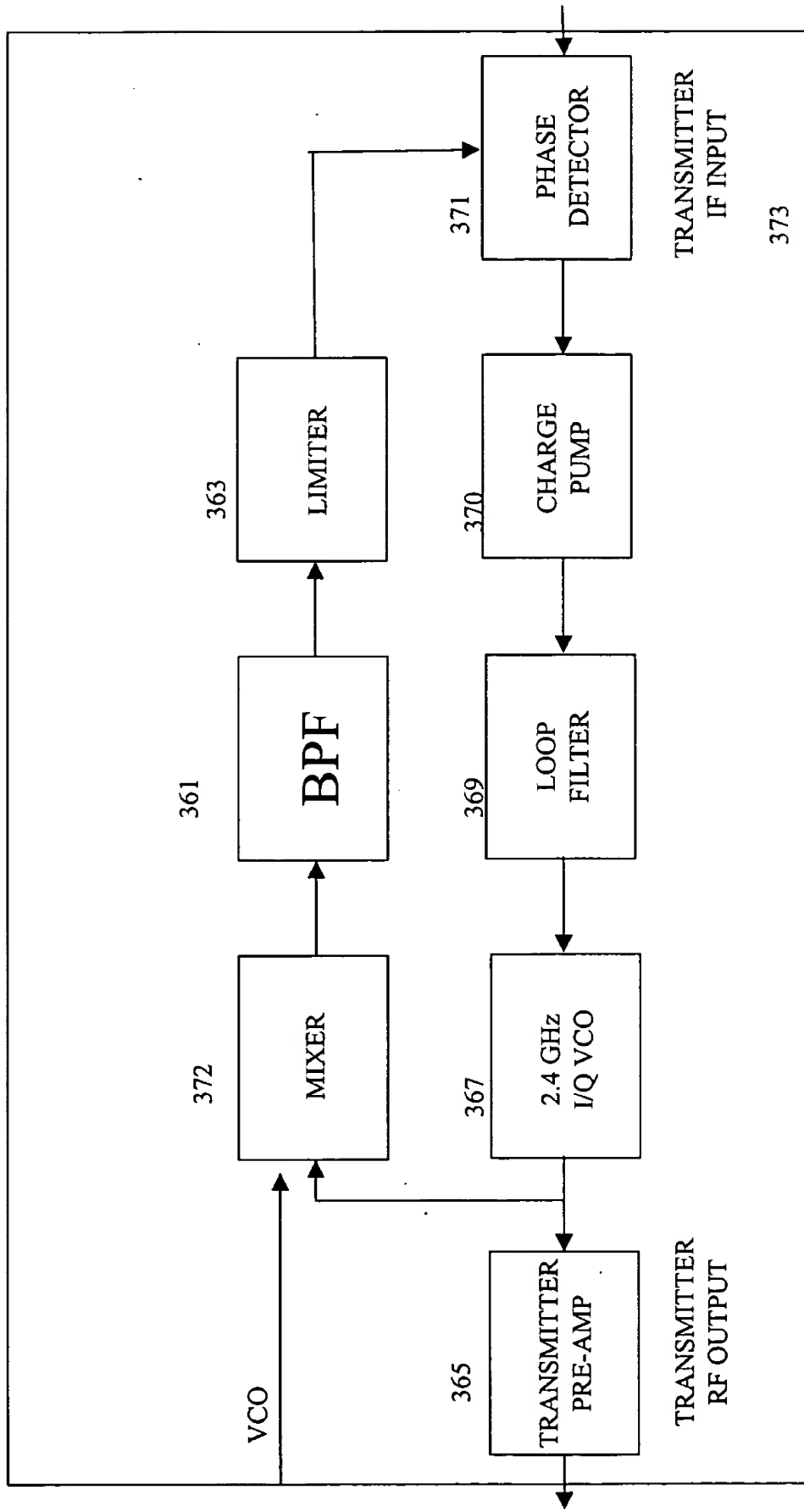
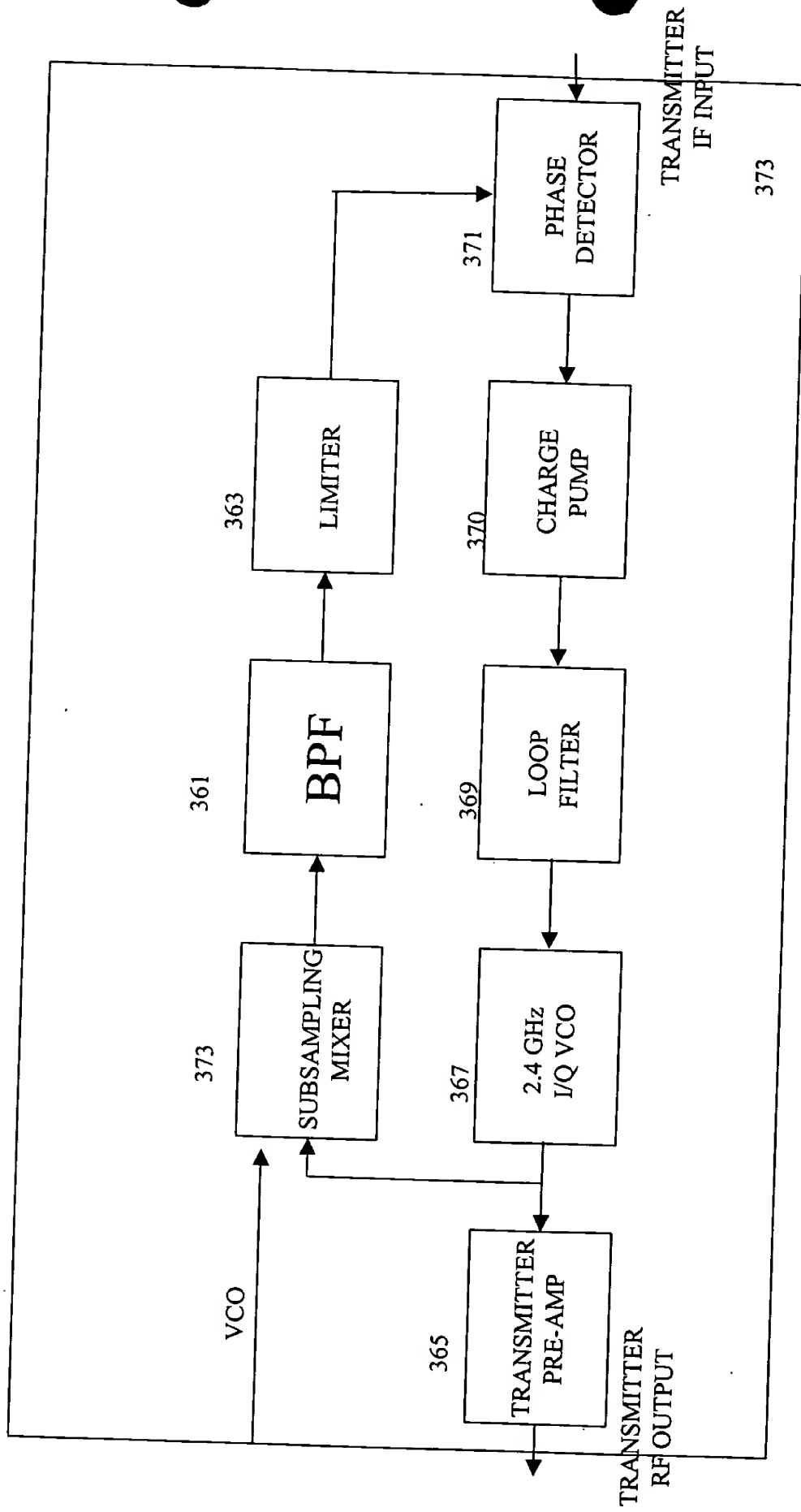


FIG. 30c

US 6,800,000 B2



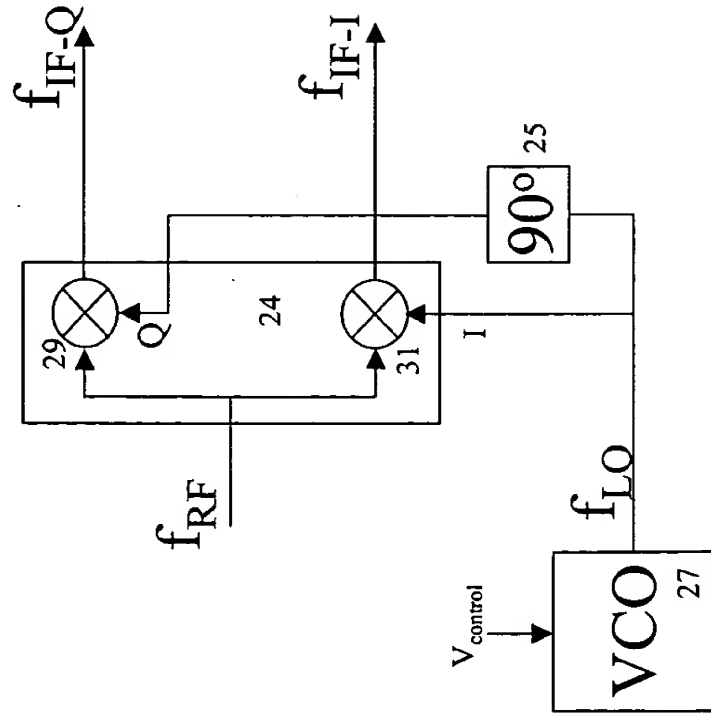
[illegible]

FIG. 302

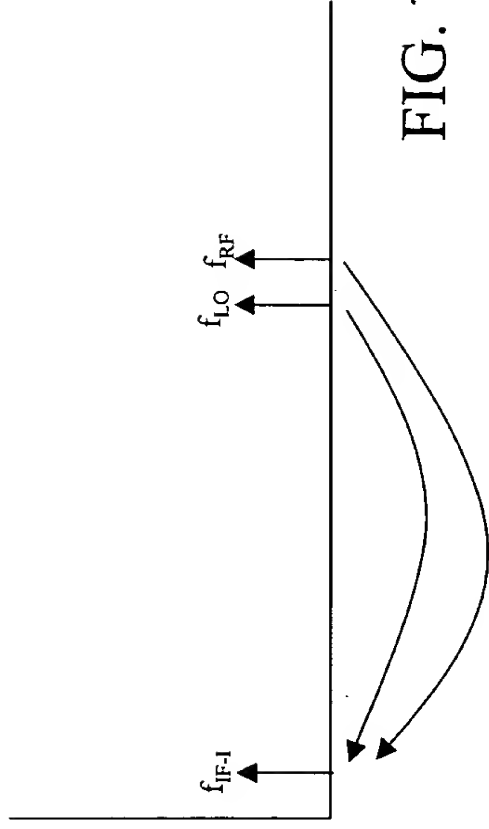


FIG. 304

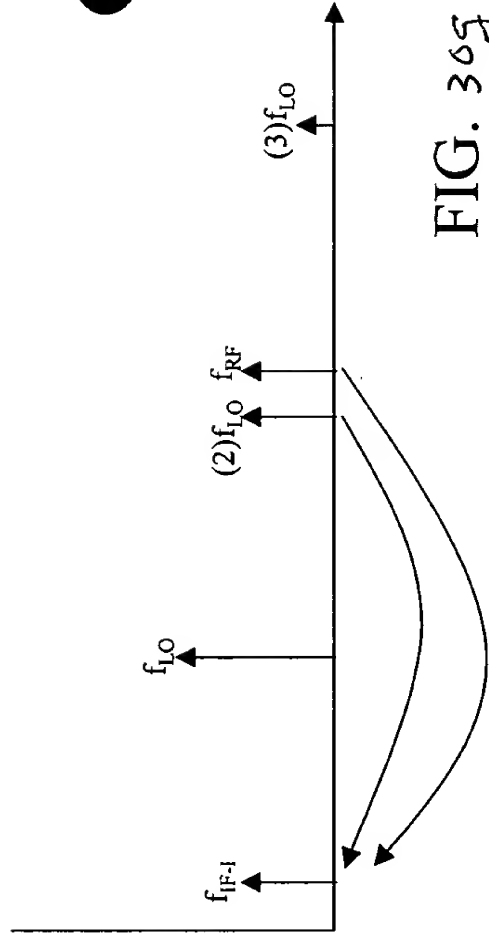


FIG. 30g

FIG. 30A

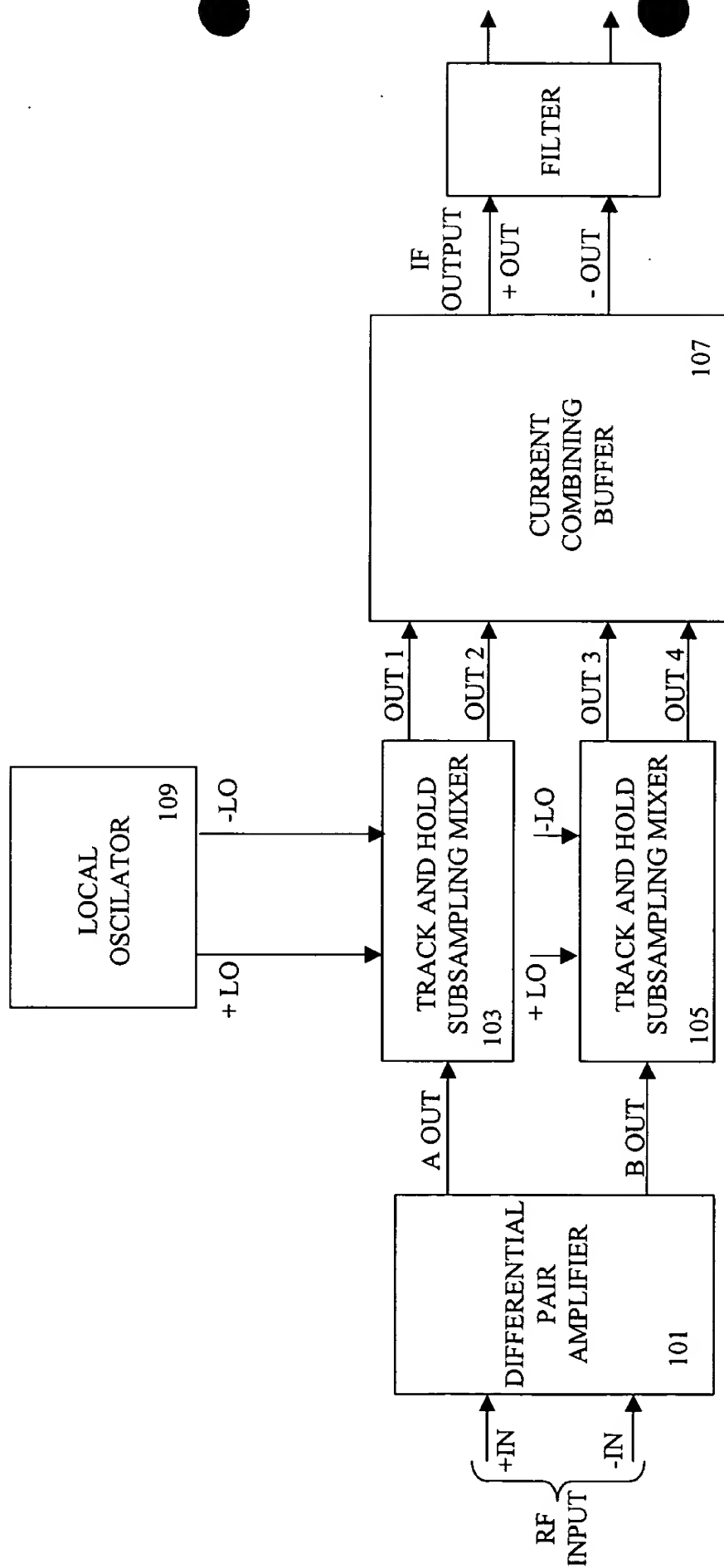


FIG. 30A

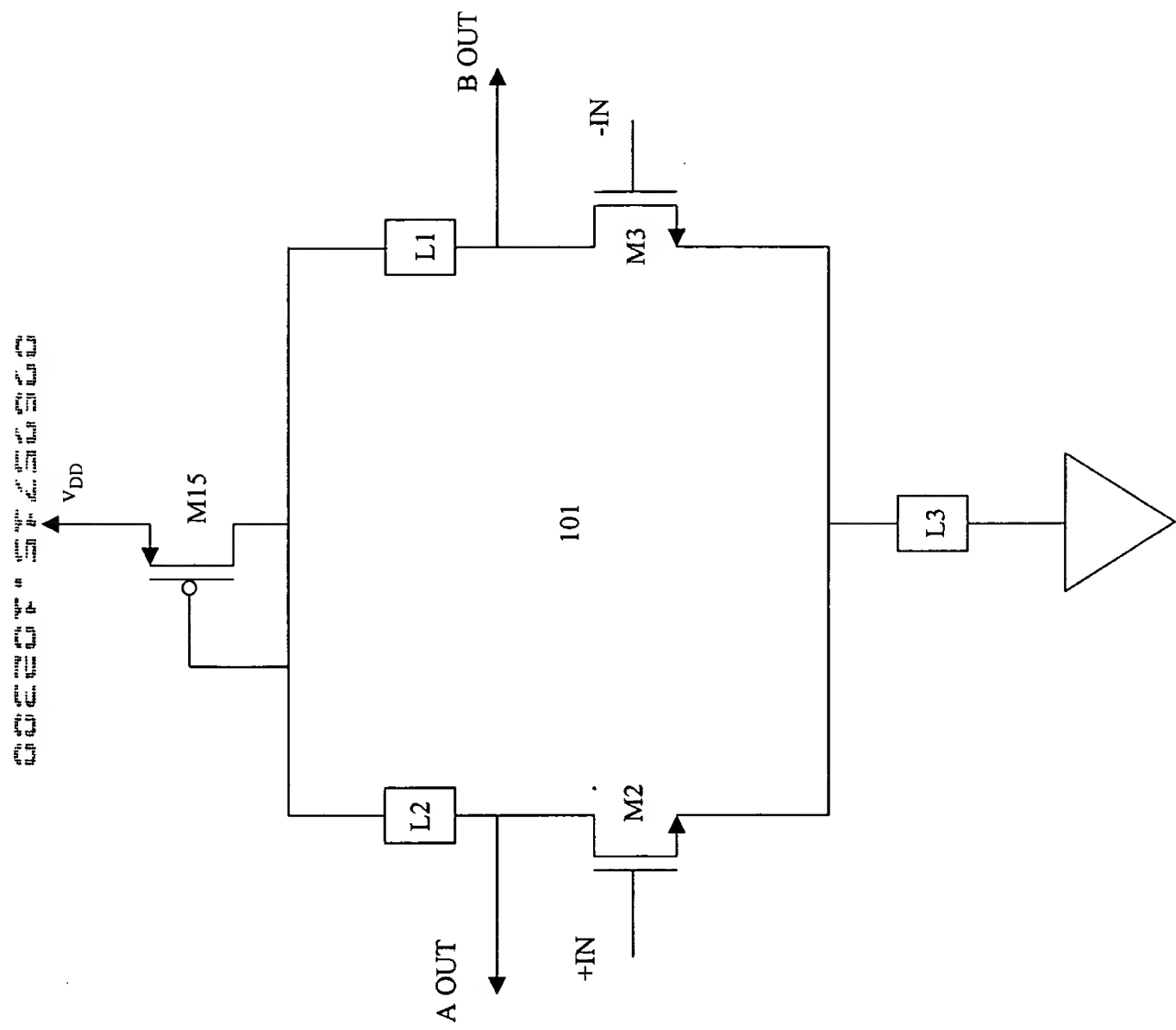


FIG. 30a

FIG. 30j

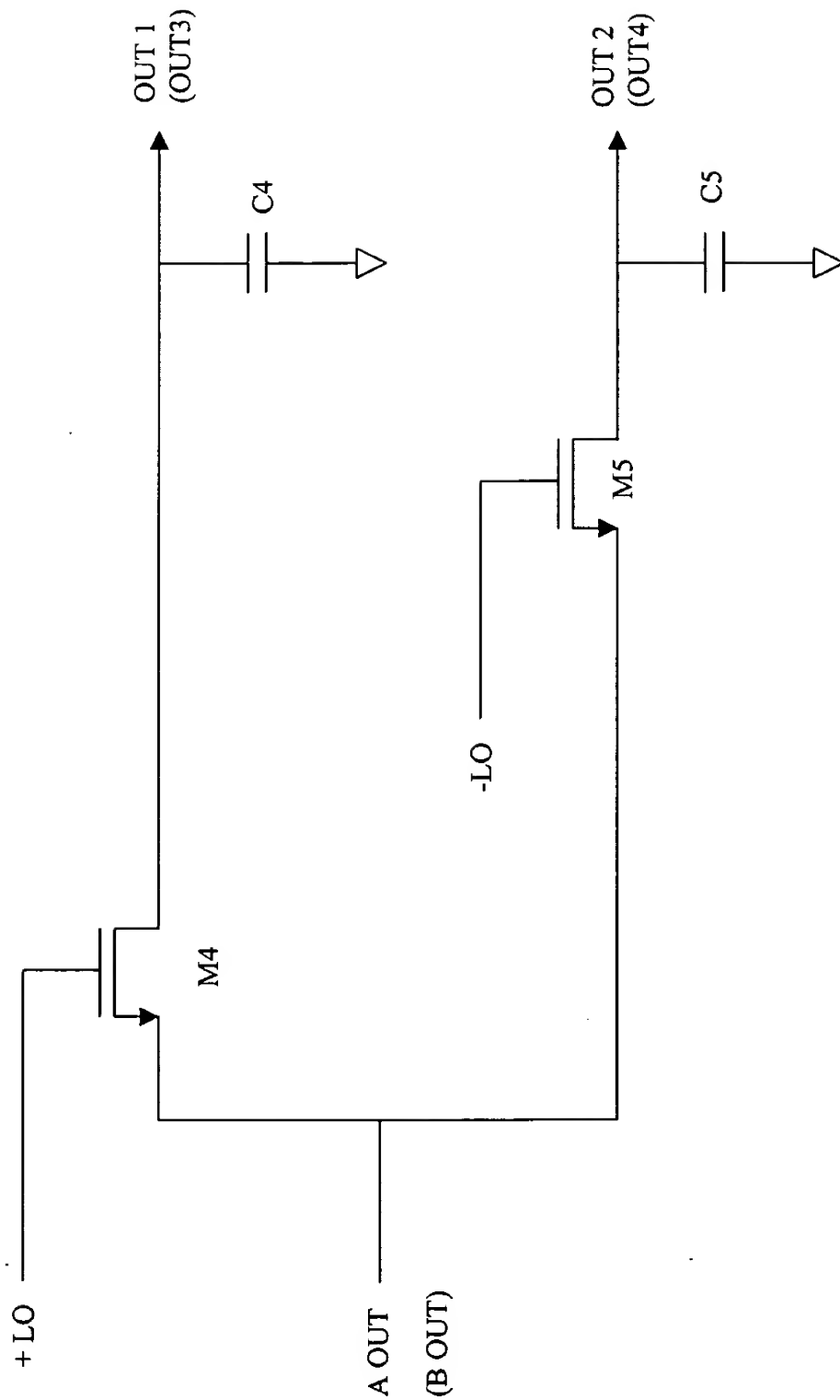


FIG. 30j

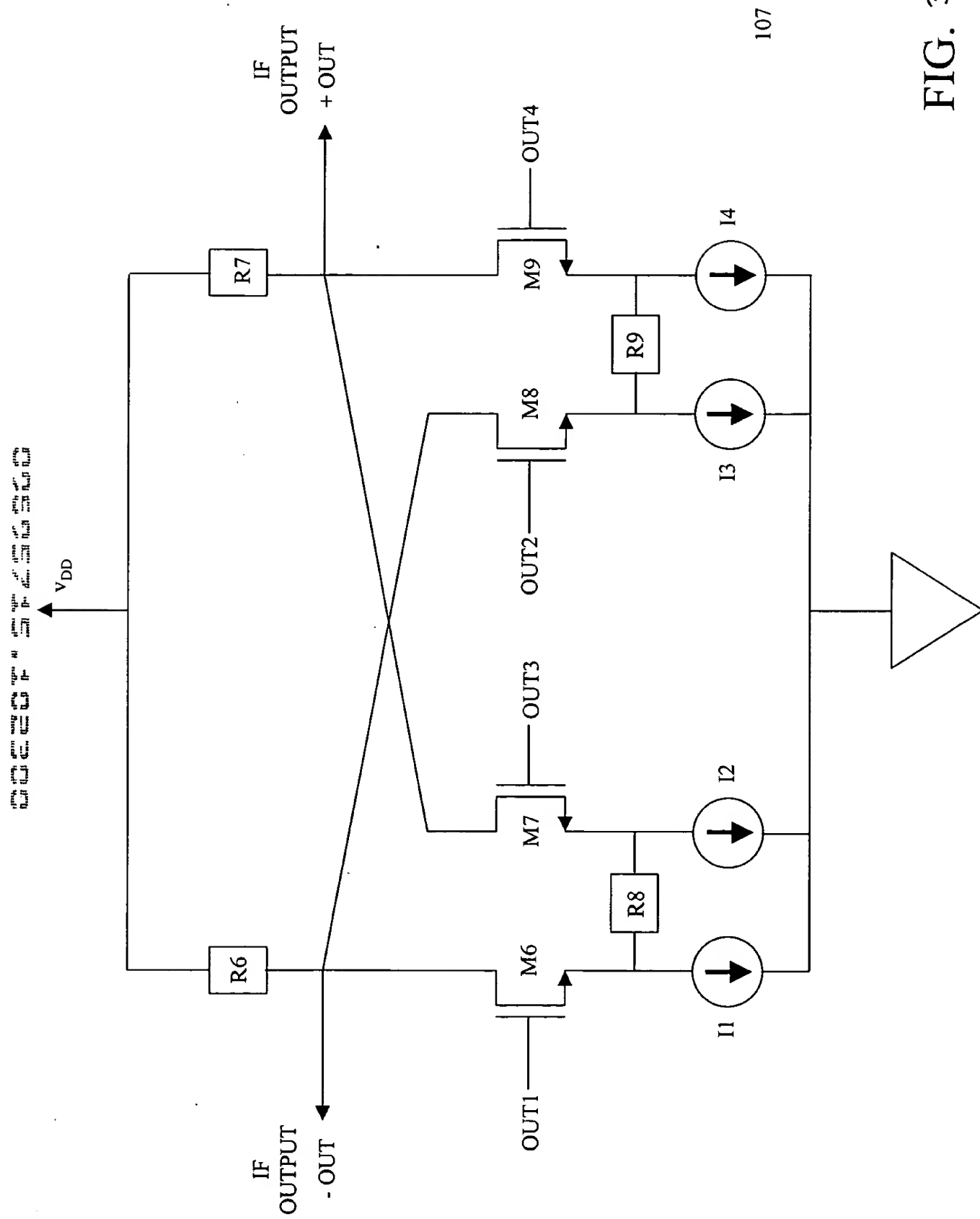


FIG. 30k

FIG. 30L

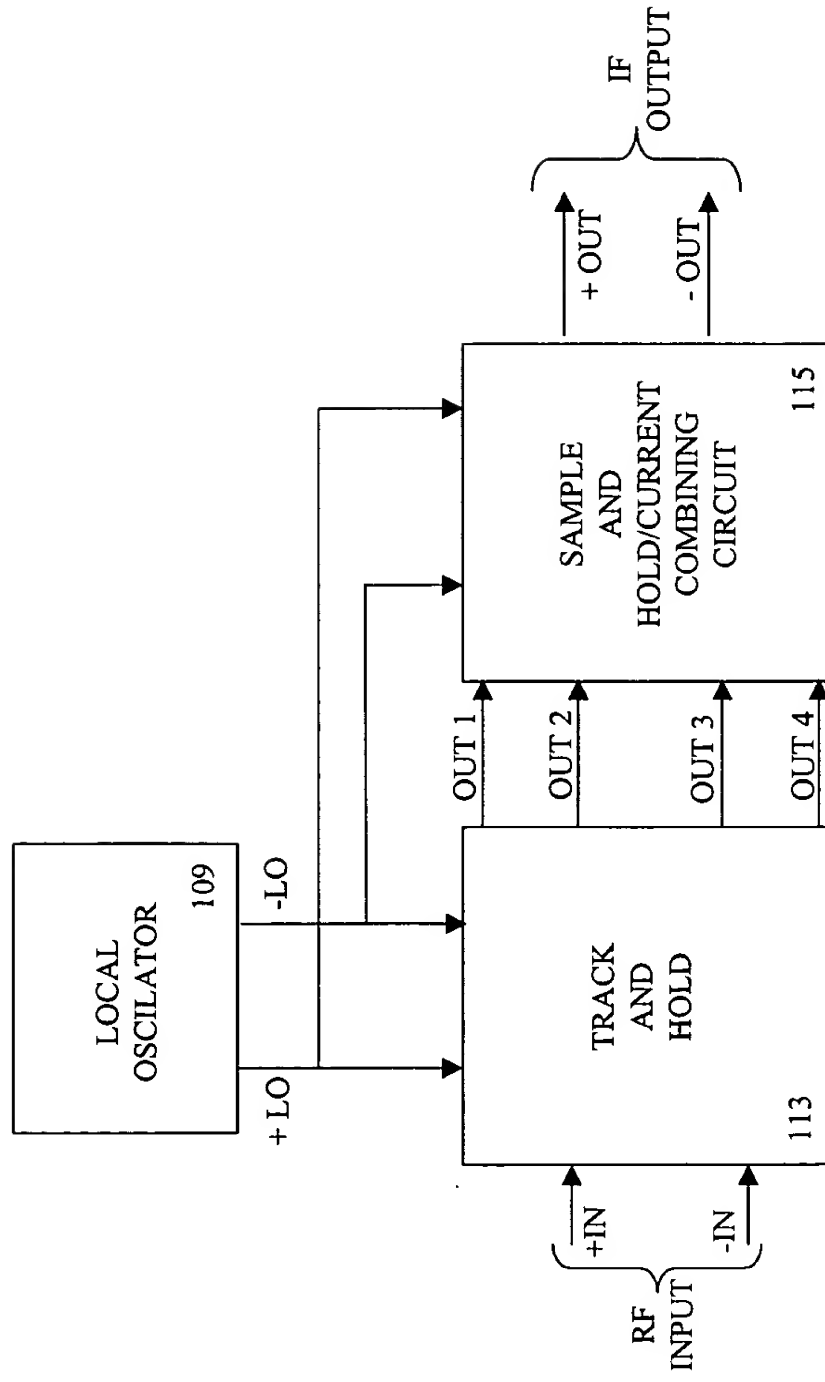
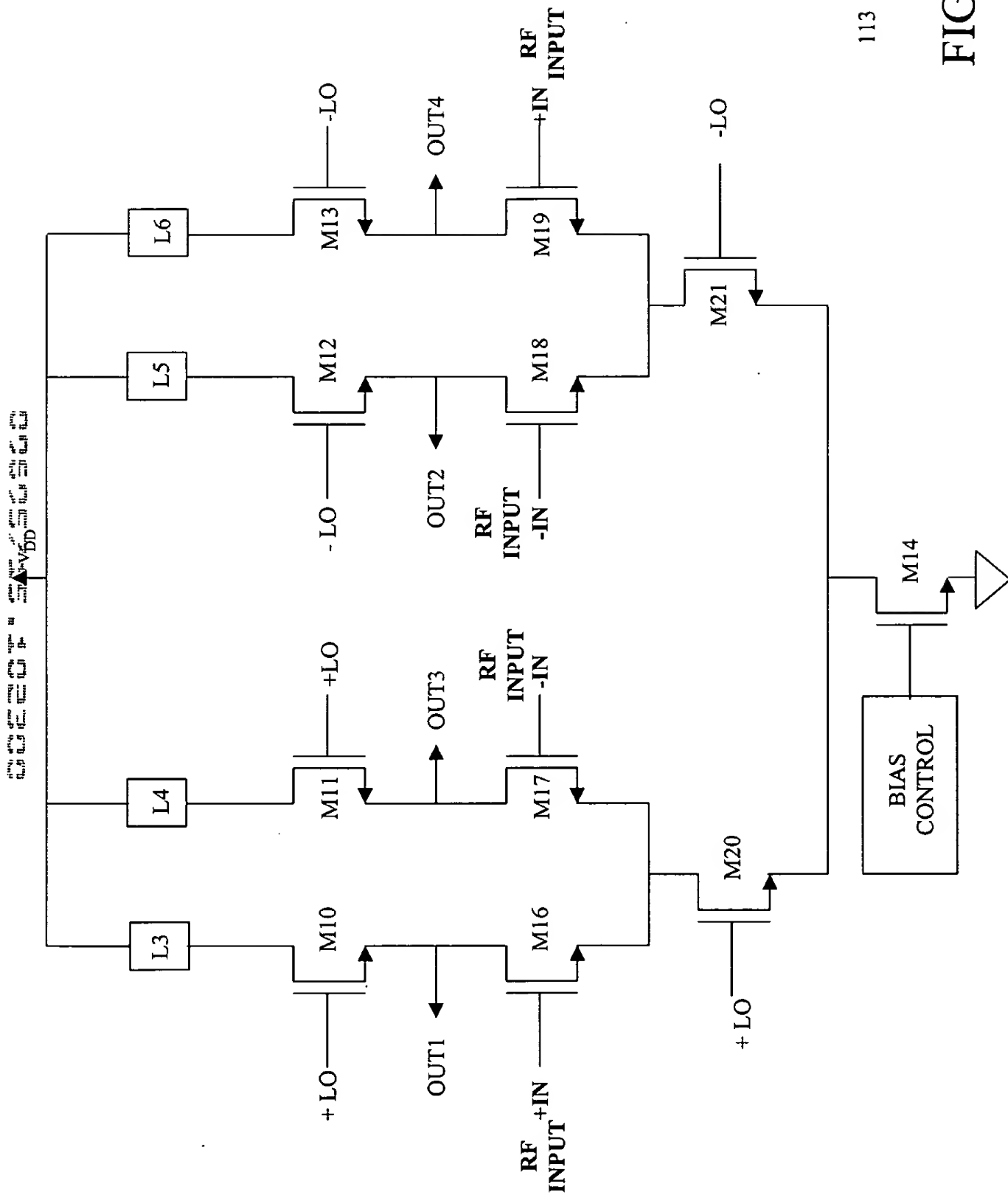


FIG. 30L



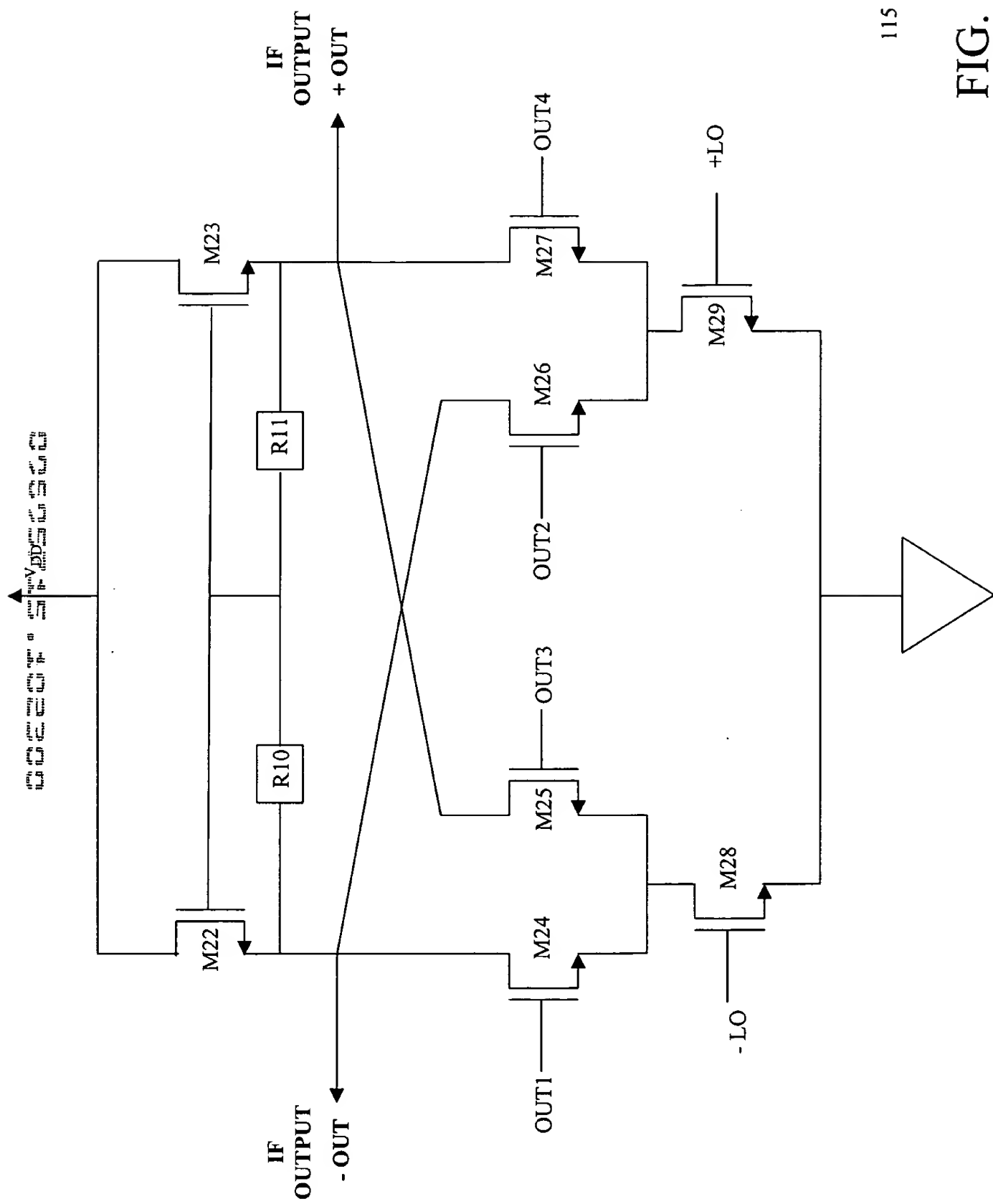


FIG. 30W

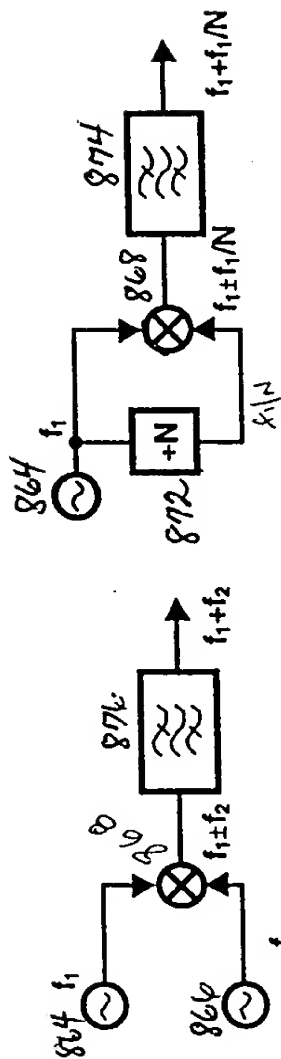


FIG. 31(a)

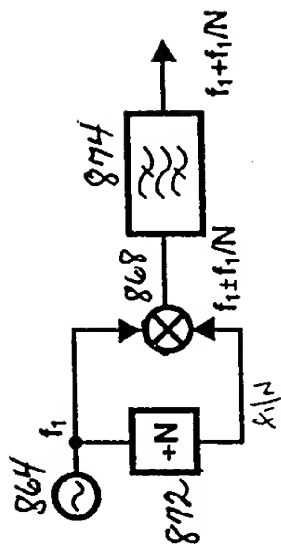


FIG. 31(b)

2025 RELEASE UNDER E.O. 14176

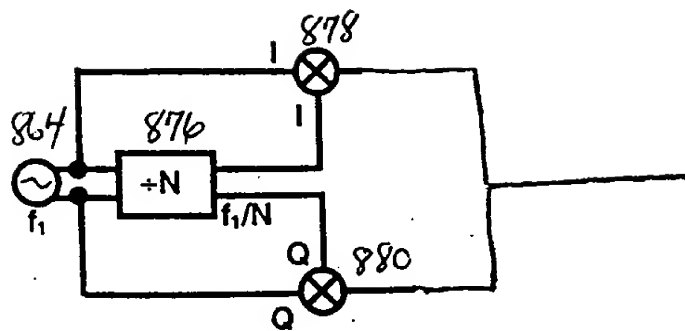


FIG. 32

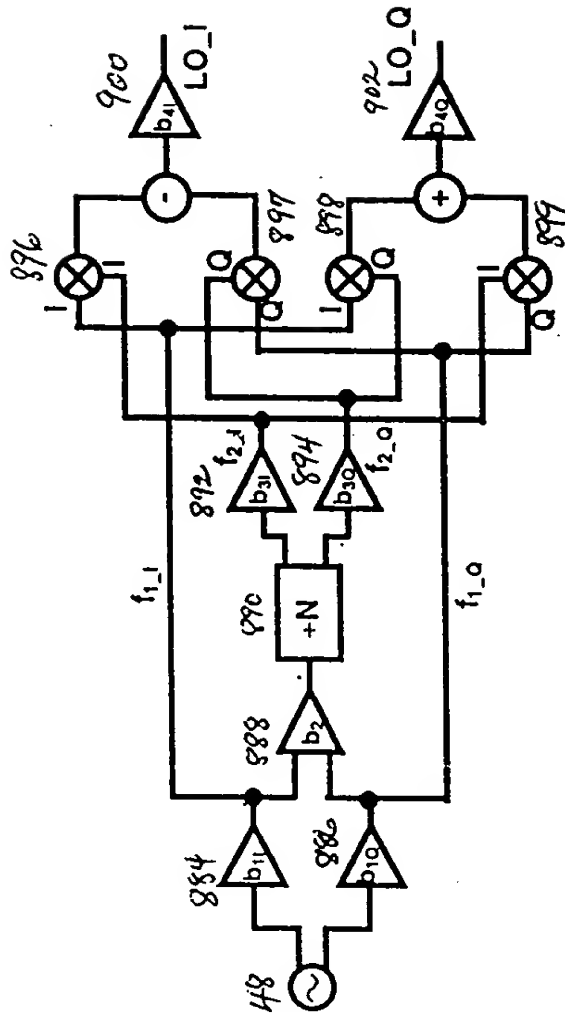


FIG. 33(a)

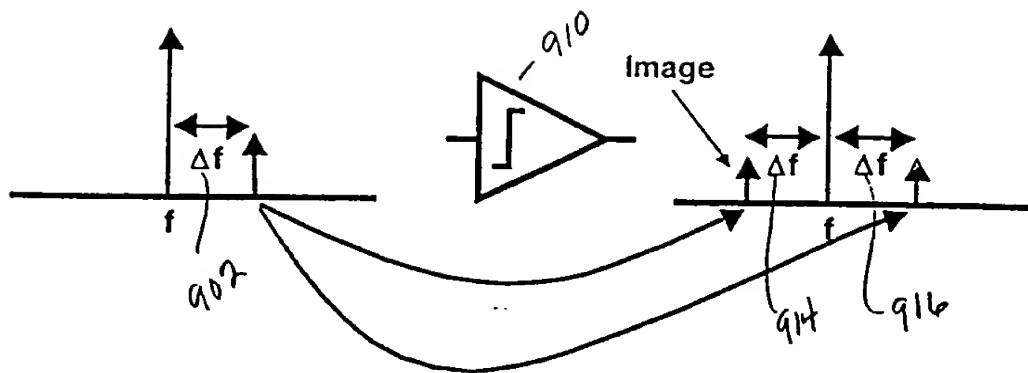


FIG. 33(b)

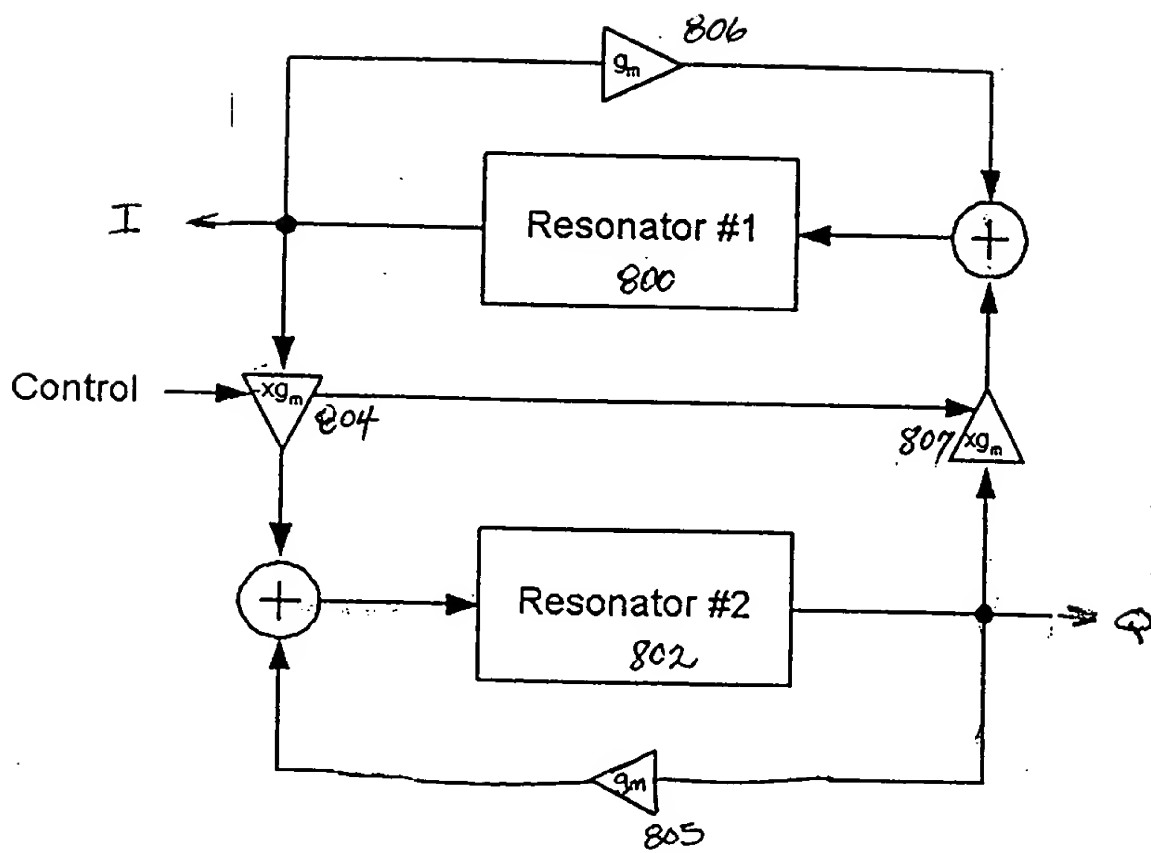


FIG. 34

FIG. 37(a)

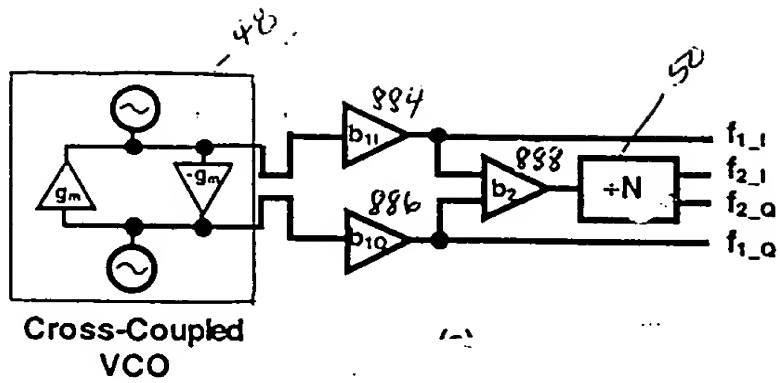
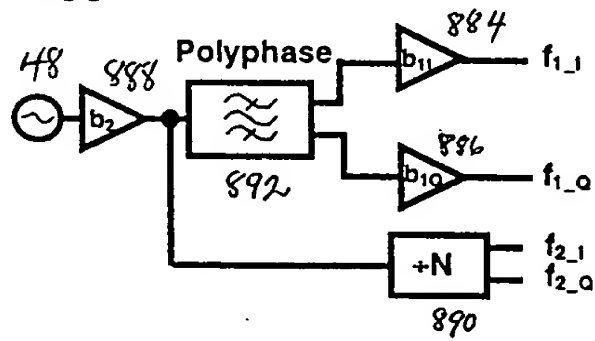


FIG. 37(b)



CONFIDENTIAL

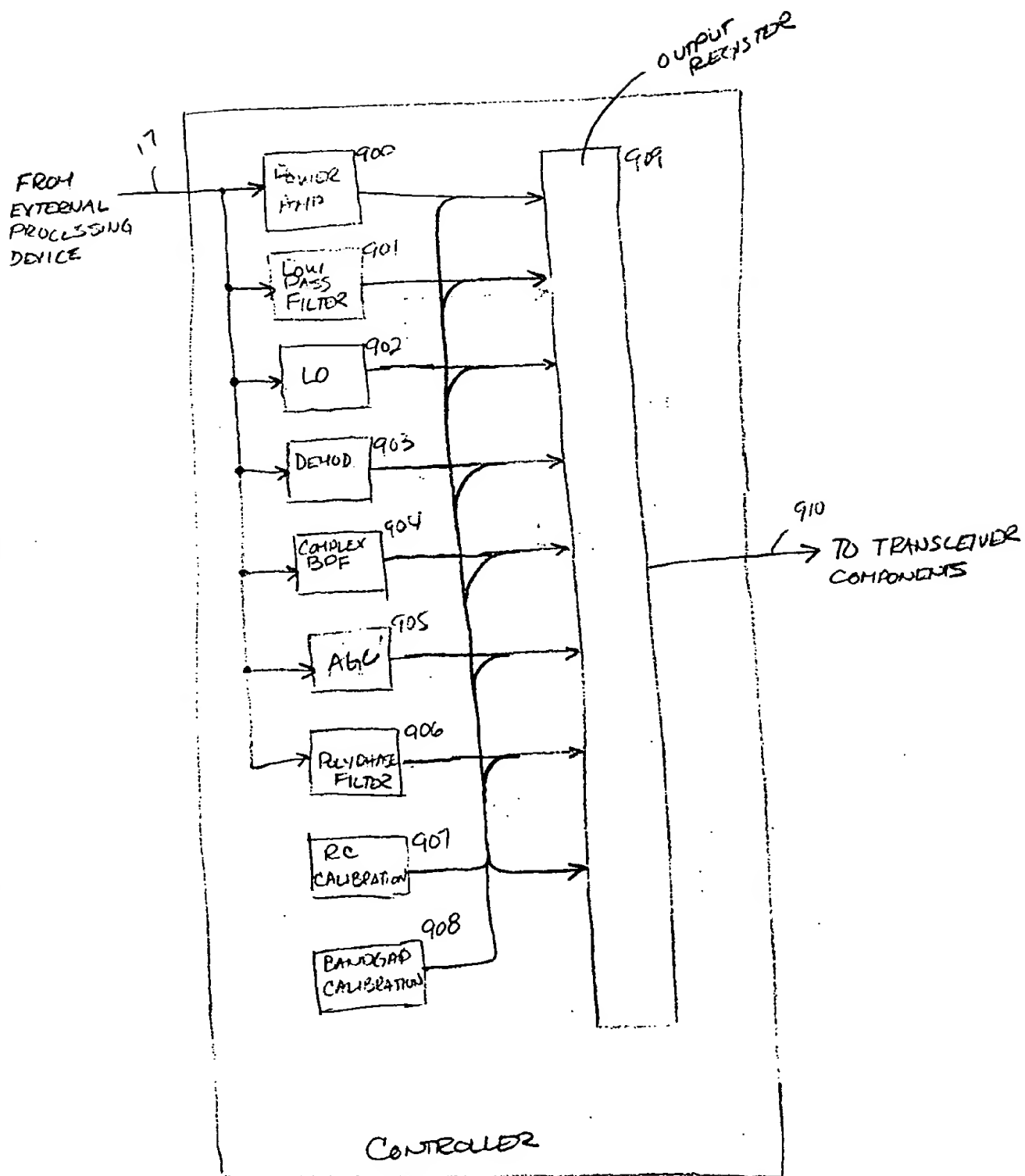


FIGURE 38

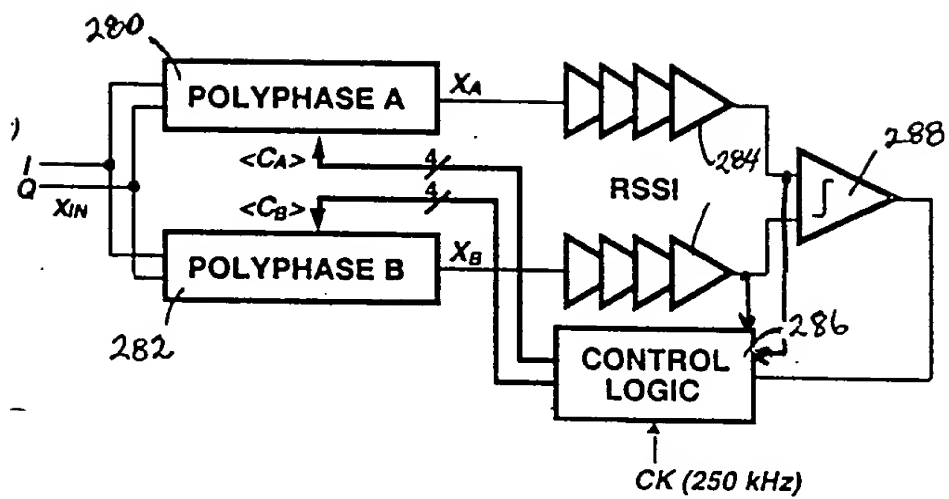


FIG. 40

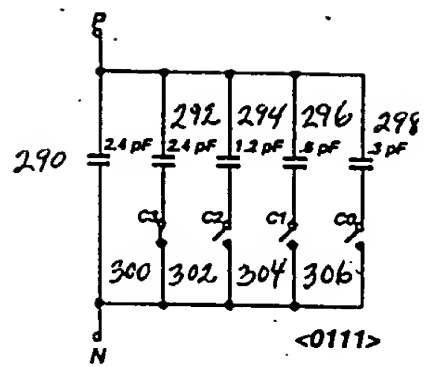


FIG. 41

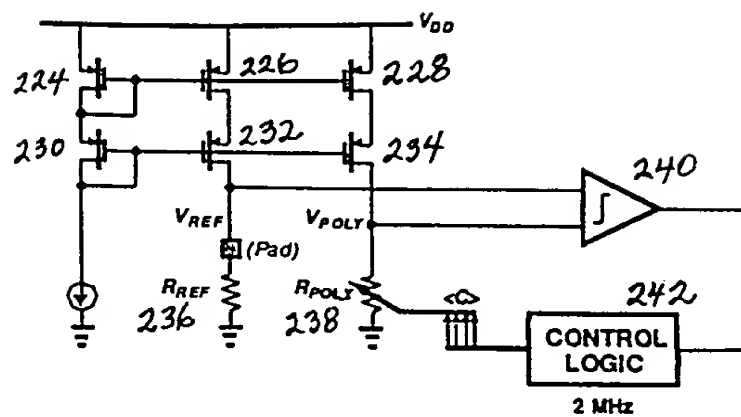


FIG. 42

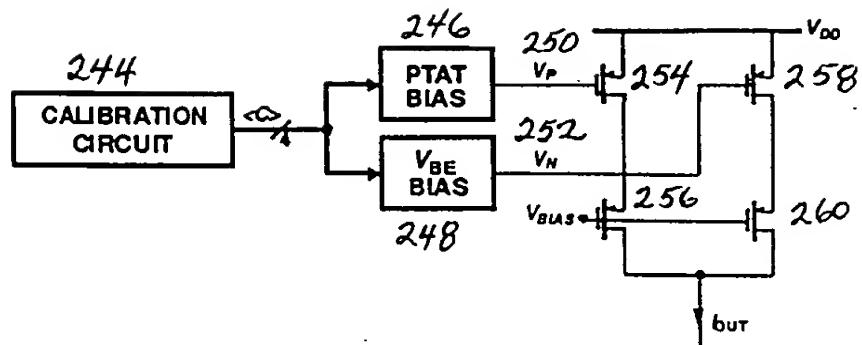


FIG. 43

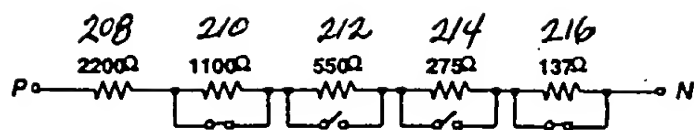


FIG. 44

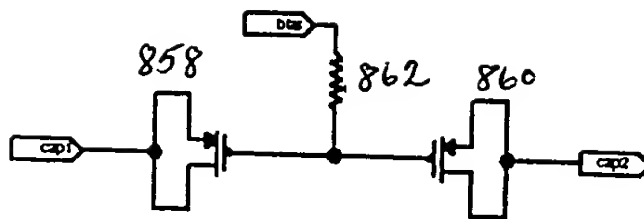
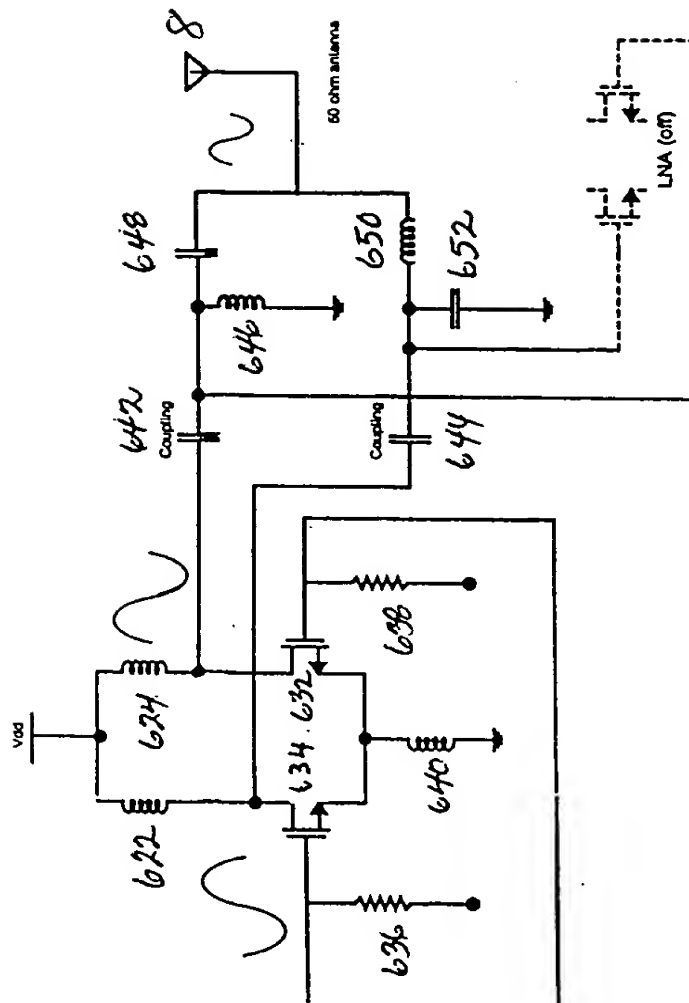


FIG. 45



File # 49

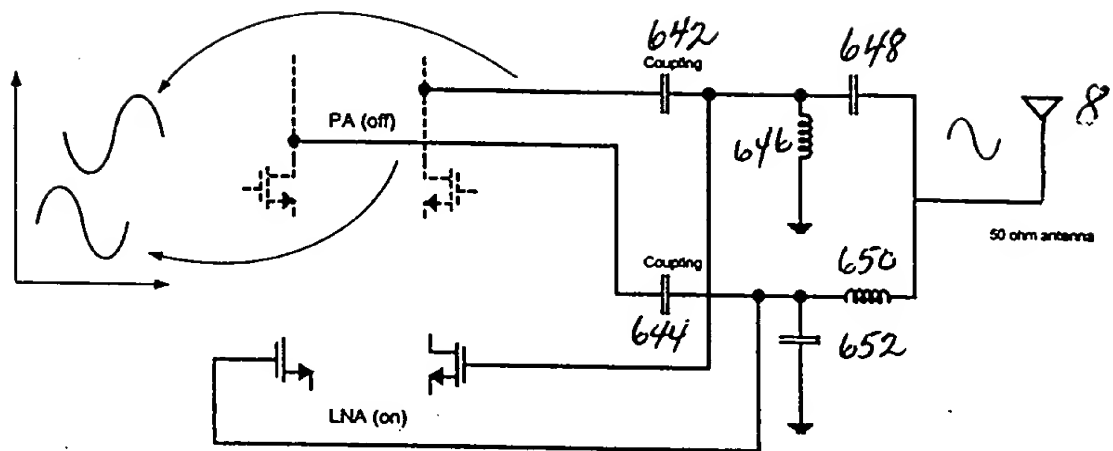


FIG. 47

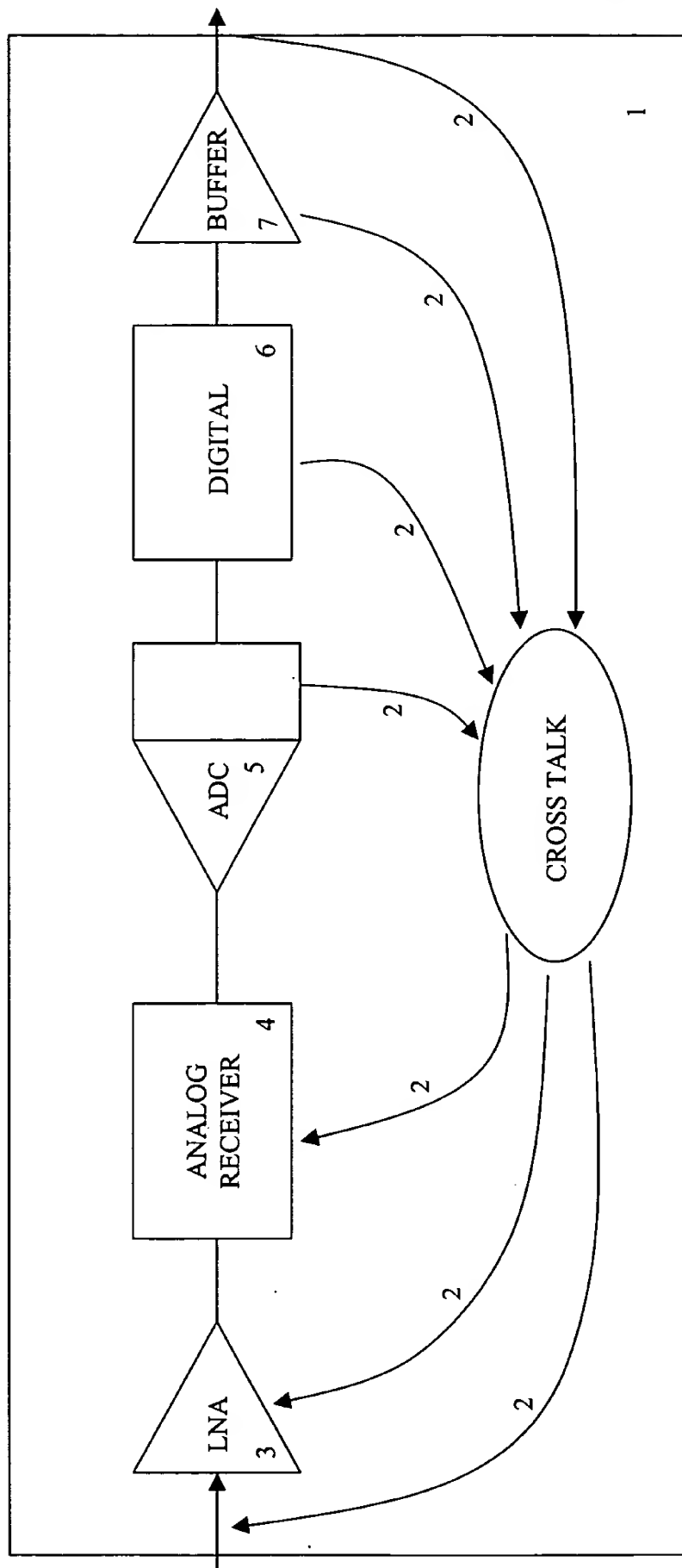


FIG. 48a

FIG. 406

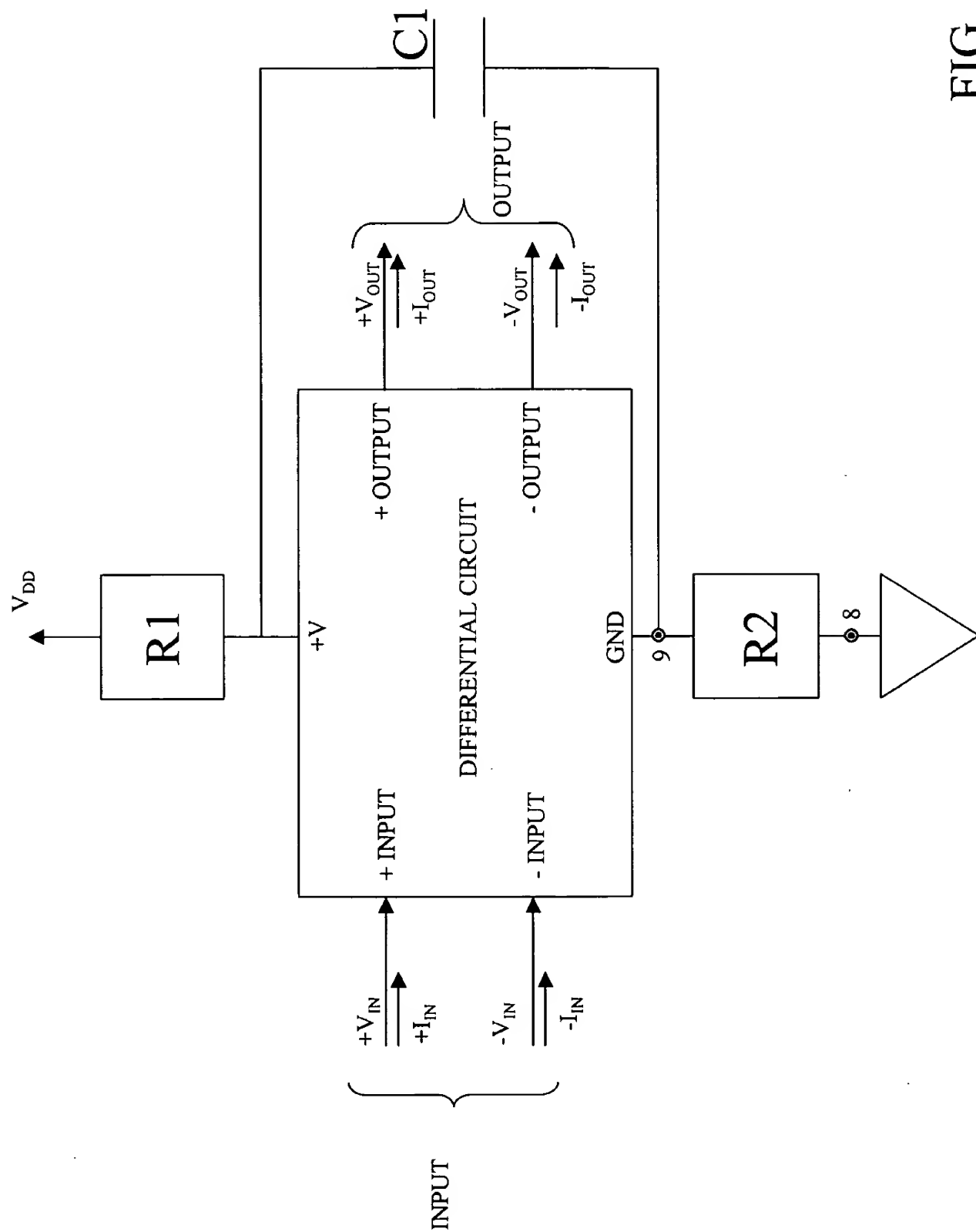


FIG. 406

U.S. PAT. & TM. OFFICE

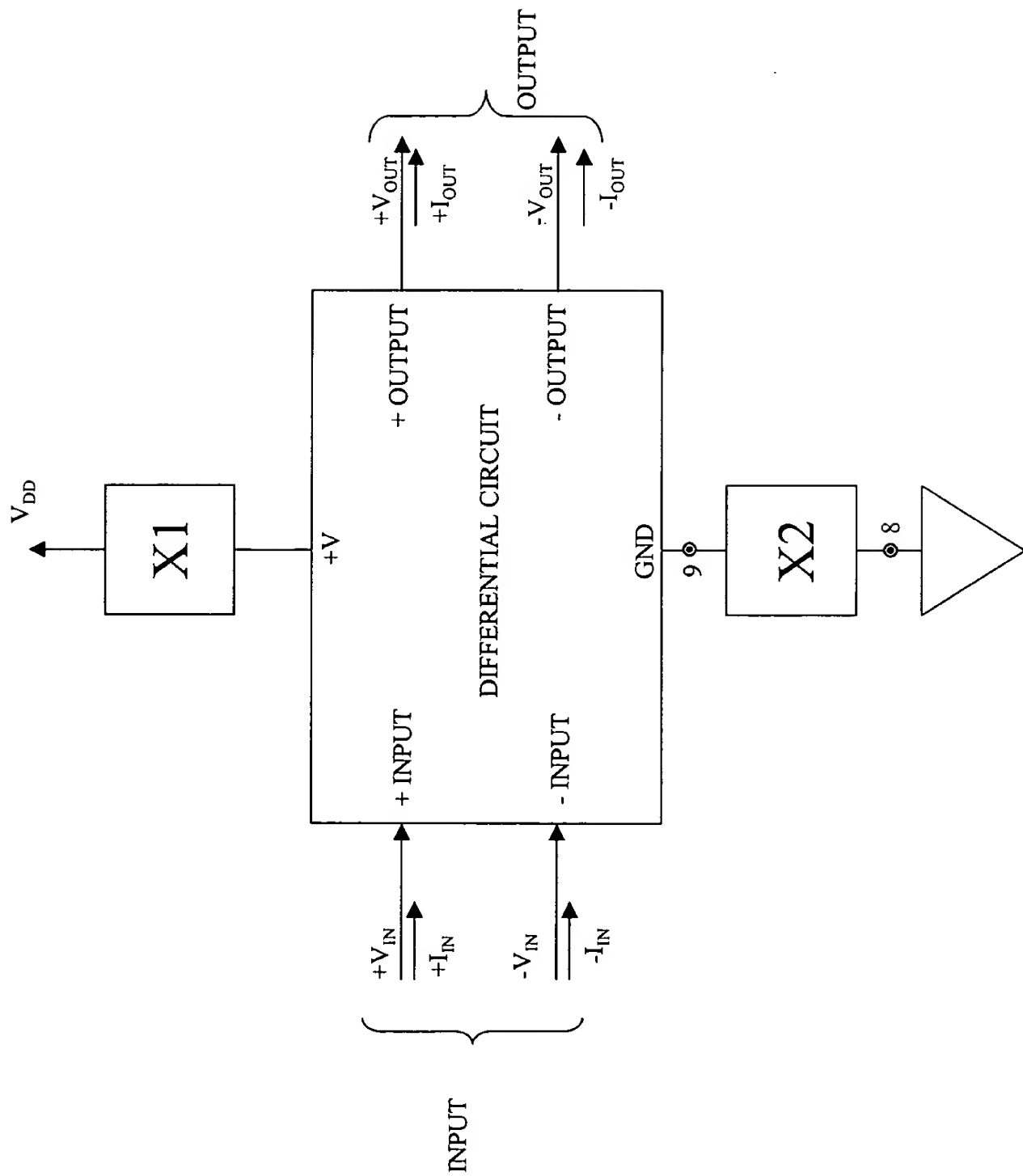


FIG. 48c

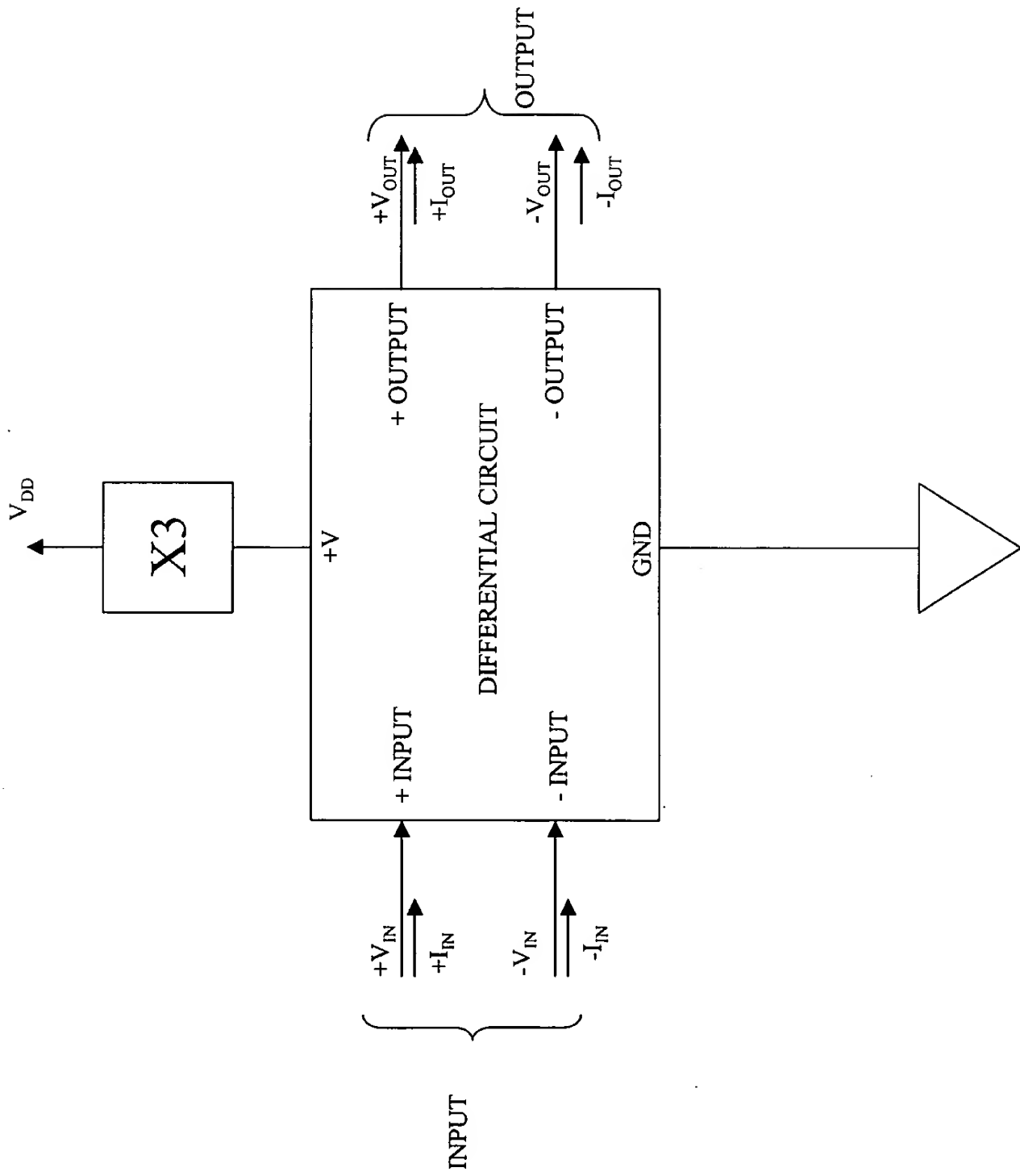


FIG. 48d

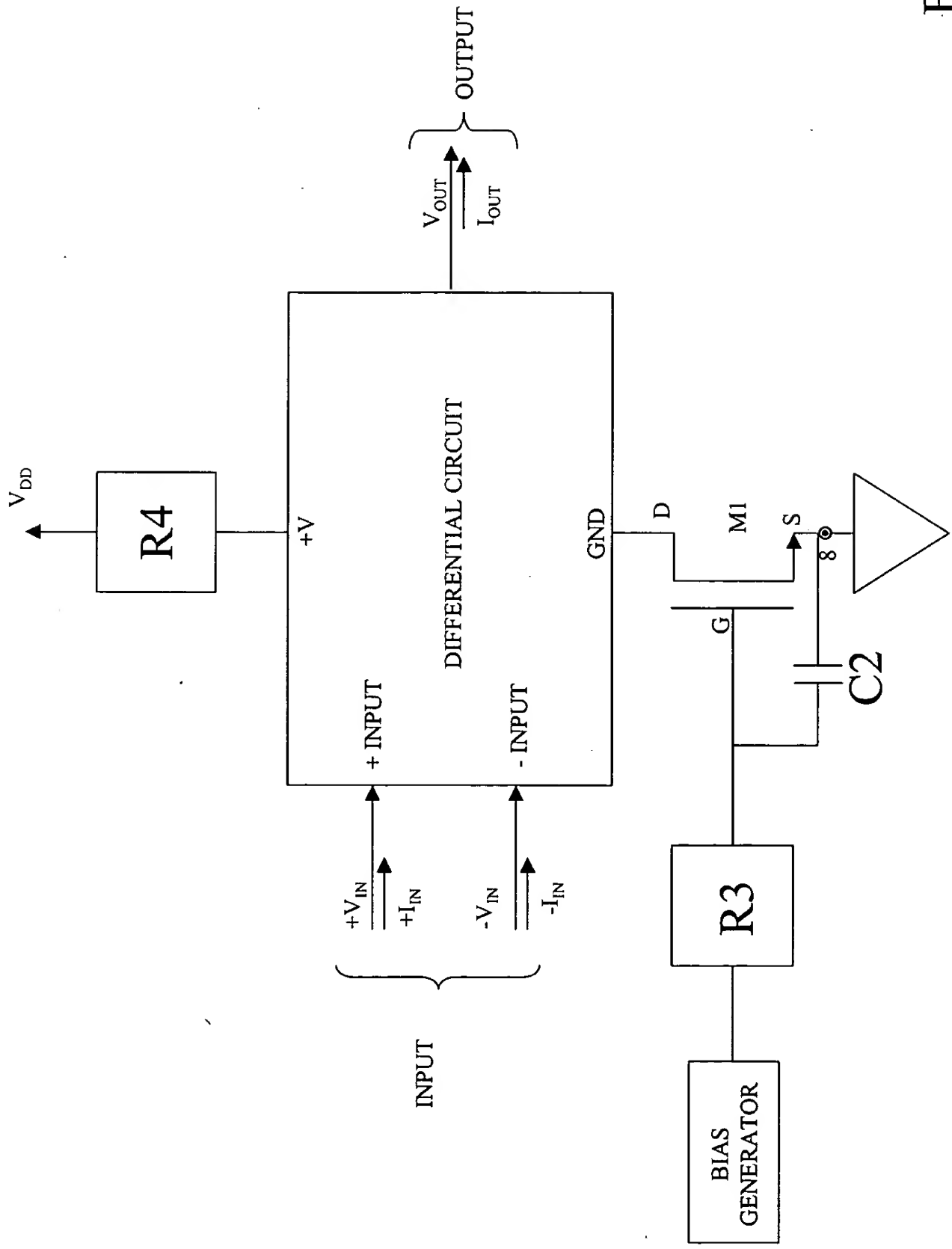


FIG. 40e

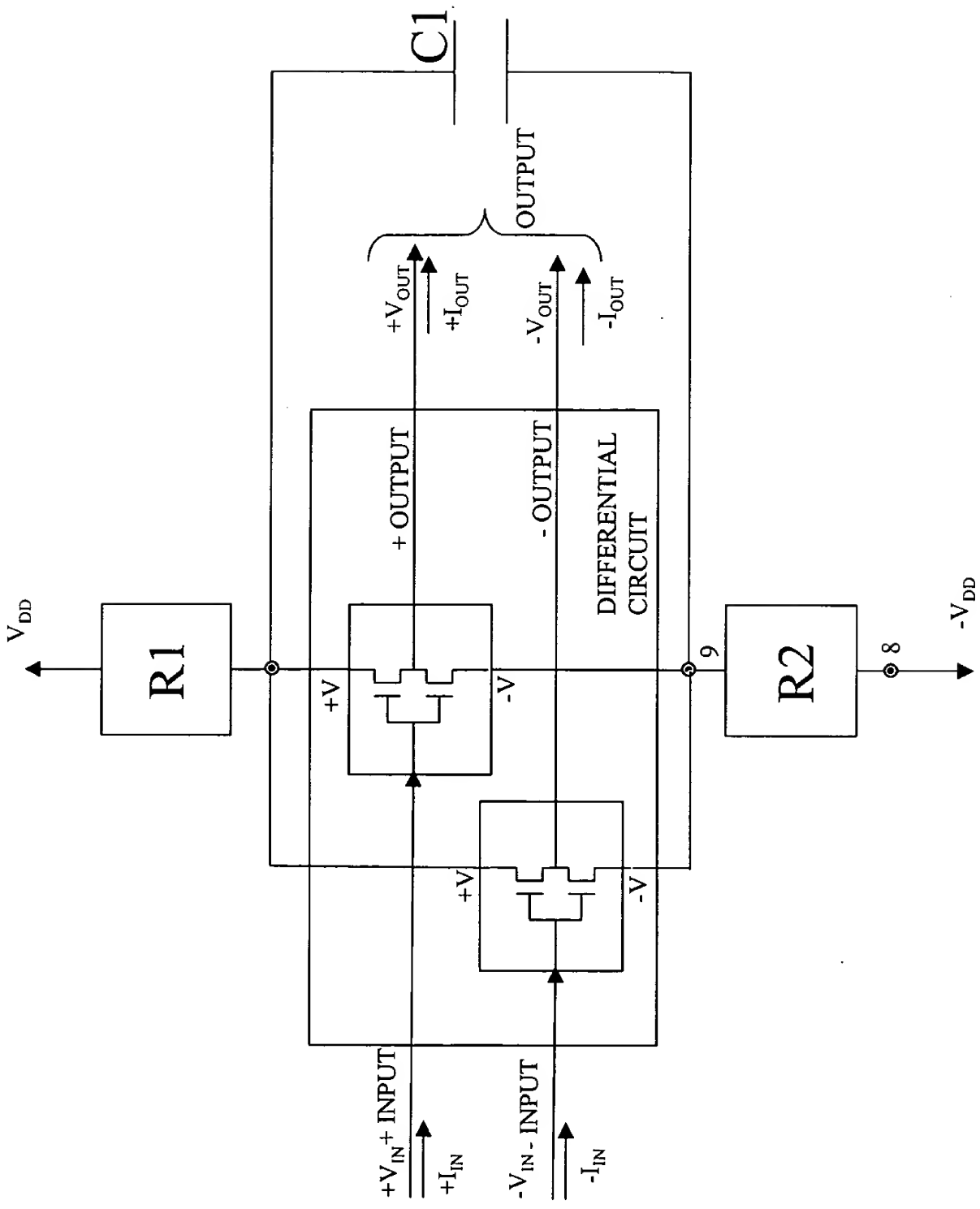


FIG. 48f

**This Page is Inserted by IFW Indexing and Scanning
Operations and is not part of the Official Record**

BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images include but are not limited to the items checked:

- ☐ BLACK BORDERS
- ☐ IMAGE CUT OFF AT TOP, BOTTOM OR SIDES
- ☒ FADED TEXT OR DRAWING
- ☐ BLURRED OR ILLEGIBLE TEXT OR DRAWING
- ☐ SKEWED/SLANTED IMAGES
- ☐ COLOR OR BLACK AND WHITE PHOTOGRAPHS
- ☐ GRAY SCALE DOCUMENTS
- ☐ LINES OR MARKS ON ORIGINAL DOCUMENT
- ☐ REFERENCE(S) OR EXHIBIT(S) SUBMITTED ARE POOR QUALITY
- ☐ OTHER: _____

IMAGES ARE BEST AVAILABLE COPY.

As rescanning these documents will not correct the image problems checked, please do not report these problems to the IFW Image Problem Mailbox.